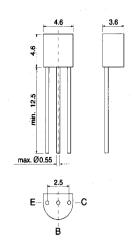
PNP Silicon Expitaxial Planar Transistor

for switching and amplifier applications. Especially suitable for AF-driver stages and low power output stages.

The transistor is subdivided into two groups, G and H, according to its DC current gain. As complementary type the NPN transistor HN 9013 is recommended.

On special request, these transistors can be manufactured in different pin configurations. Please refer to the "TO-92 TRANSISTOR PACKAGE OUTLINE" on page 80 for the available pin options.



TO-92 Plastic Package Weight approx. 0.18 g Dimensions in mm

Absolute Maximum Ratings

	Symbol	Value	Unit	
Collector Emitter Voltage	-VCEO	30	V	
Emitter Base Voltage	-V _{EBO}	5	V	
Collector Current	-lc	800	mA	
Peak Collector Current	-I _{CM}	1	А	
Base Current	-l _B	100	mA	
Power Dissipation at T _{amb} = 25 °C	P _{tot}	625 ¹⁾	mW	
Junction Temperature	Tj	150	°C	
Storage Temperature Range	T _S	-55 to +150	°C	
1) Valid provided that leads are kept at ambient tem	perature at a distanc	e of 2 mm from case	Э	

GSPFORMAAVAILABLE





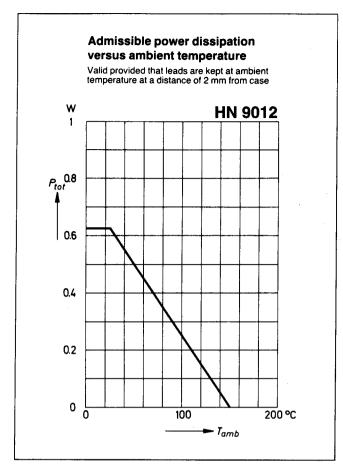
Characteristics at T_{amb} = 25 °C

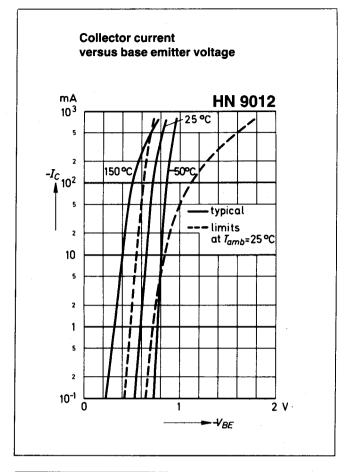
	Symbol	Min.	Тур.	Max.	Unit	
DC Current Gain at -V _{CE} = 1 V, -I _C = 50 mA Current Gain Group G	h _{FE}	110 177		183 250	-	
at -V _{CE} = 1 V, -I _C = 500 mA	h _{FE}	40	-	-		
Collector Cutoff Current at -V _{CB} = 31 V	-Ісво	-	-	100	nA	
Collector Emitter Breakdown Voltage at -I _C = 1 mA	-V _{(BR)CEO}	30	-	-	V	
Emitter Base Cutoff Current at $-V_{EB} = 5.1 \text{ V}$	-l _{EBO}	- -	-	100	nA	
Collector Saturation Voltage at $-I_C = 500 \text{ mA}$, $-I_B = 20 \text{ mA}$	-V _{CEsat}	-	*	0.7	V	
Base Saturation Voltage at -I _C = 500 mA, -I _B = 20 mA	-V _{BEsat}	* -	-	1.2	V	
Base Emitter Voltage at $-V_{CE} = 1 \text{ V, } -I_{C} = 50 \text{ mA}$	-V _{BE}	0.6	-	0.75	V	
Gain Bandwidth Product at $-V_{CE} = 5 \text{ V}$, $-I_{C} = 10 \text{ mA}$, $f = 50 \text{ MHz}$	f _T	-	100	_	MHz	
Collector Base Capacitance at -V _{CB} = 10 V, f = 1 MHz	C _{CBO}	-	12	-	pF	
Thermal Resistance Junction to Ambient	R _{thA}	-	-	200 ¹⁾	K/W	
1) Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case						

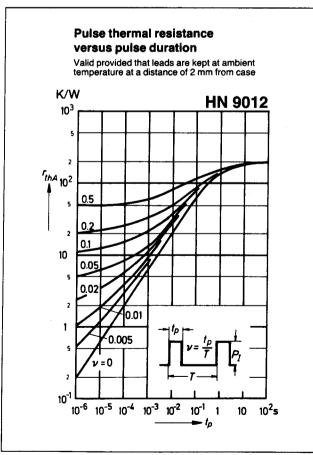
GSPFORMAAVAILABLE

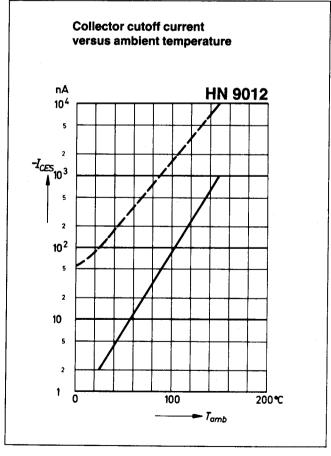






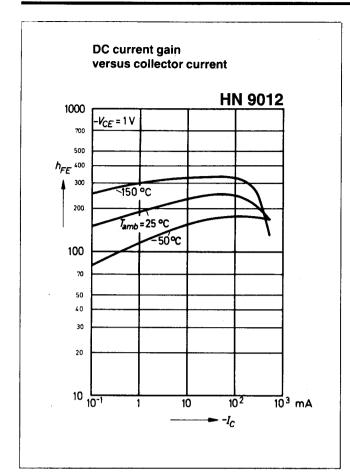


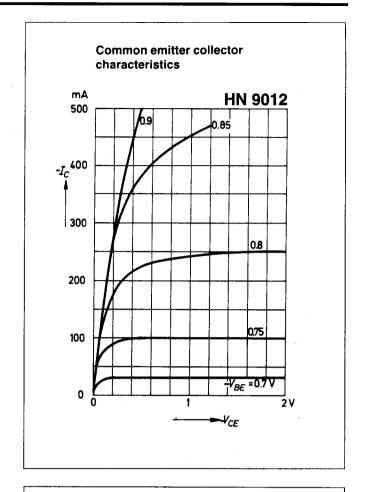


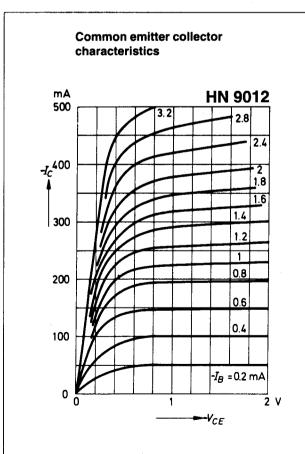


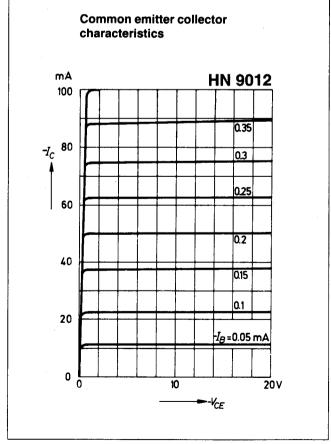




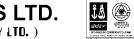


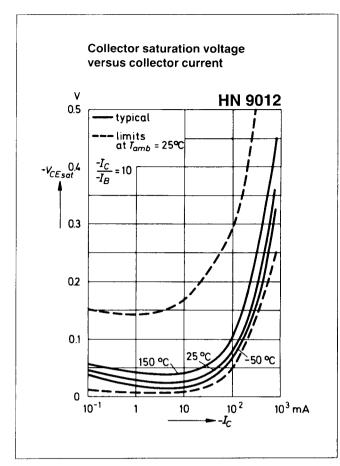


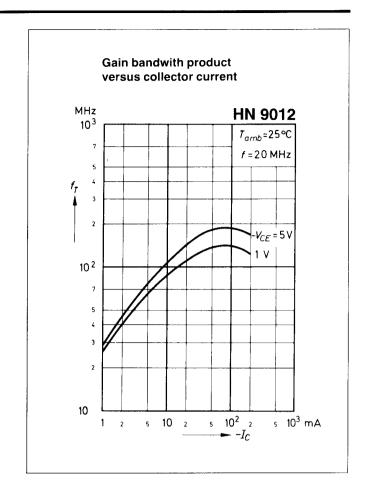


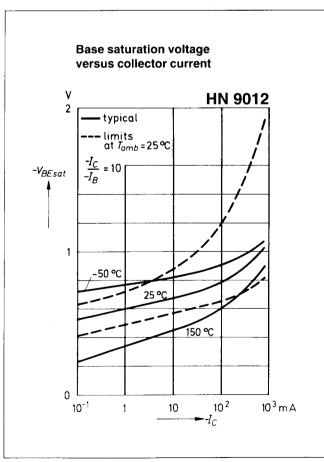
















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