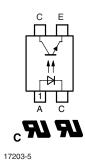


### Vishay Semiconductors

## **Optocoupler, Phototransistor Output**





#### **DESCRIPTION**

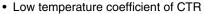
In the K817P part each channel consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin (single) plastic dual inline package.

### **AGENCY APPROVALS**

- UL1577, file no. E52744, double protection
- cUL CSA 22.2 bulletin 5A, double protection, E52744

### **FEATURES**

- Endstackable to 2.54 mm (0.1") spacing
- DC isolation test voltage 5000 V<sub>RMS</sub>
- Current transfer ratio (CTR) selected into groups





- · Available in single, dual and quad channel packages
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **APPLICATIONS**

- · Programmable logic controllers
- Modems
- · Answering machines
- · General applications

ORDER INFORMATION				
PART	REMARKS			
K817P	CTR 50 % to 600 %, DIP-4			
K817P1	CTR 40 % to 80 %, DIP-4			
K817P2	CTR 63 % to 125 %, DIP-4			
K817P3	CTR 100 % to 200 %, DIP-4			
K817P4	CTR 160 % to 320 %, DIP-4			
K817P5	CTR 50 % to 150 %, DIP-4			
K817P6	CTR 100 % to 300 %, DIP-4			
K817P7	CTR 80 % to 160 %, DIP-4			
K817P8	CTR 130 % to 260 %, DIP-4			
K817P9	CTR 200 % to 400 %, DIP-4			

ABSOLUTE MAXIMUM RATINGS (1)							
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL VALUE				
INPUT							
Reverse voltage		$V_{R}$	6	V			
Forward current		I <sub>F</sub>	60	mA			
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α			
Power dissipation		P <sub>diss</sub>	70	mW			
Junction temperature		Tj	125	°C			
OUTPUT							
Collector emitter voltage		$V_{CEO}$	70	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		Ic	50	mA			
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA			
Power dissipation		P <sub>diss</sub>	70	mW			
Junction temperature		Tj	125	°C			

## Vishay Semiconductors Optocoupler, Phototransistor Output



ABSOLUTE MAXIMUM RATINGS (1)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
COUPLER	COUPLER								
AC isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub>	5000	$V_{RMS}$					
Total power dissipation		P <sub>tot</sub>	200	mW					
Operating ambient temperature range		T <sub>amb</sub>	- 40 to + 100	°C					
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C					
Soldering temperature (2)	2 mm from case, t ≤ 10 s	T <sub>sld</sub>	260	°C					

#### **Notes**

<sup>(2)</sup> Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT	INPUT								
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.25	1.6	V			
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C <sub>j</sub>		50		pF			
OUTPUT									
Collector emitter voltage	$I_{C} = 100 \ \mu A$	$V_{CEO}$	70			V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V			
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I <sub>CEO</sub>			100	nA			
COUPLER									
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 1 mA	V <sub>CEsat</sub>			0.3	V			
Cut-off frequency	$I_F$ = 10 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$	f <sub>c</sub>		100		kHz			
Coupling capacitance	f = 1 MHz	Ck		0.6		pF			

### Note

 $T_{amb}$  = 25 °C, unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$V_{CE} = 5 \text{ V}, I_{F} = 5 \text{ mA}$	K817P	CTR	50		600	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	K817P1	CTR	40		80	%
		K817P2	CTR	63		125	%
		K817P3	CTR	100		200	%
		K817P4	CTR	160		320	%
l <sub>¢</sub> /l <sub>F</sub>	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 5 mA	K817P5	CTR	50		150	%
		K817P6	CTR	100		300	%
		K817P7	CTR	80		160	%
		K817P8	CTR	130		260	%
		K817P9	CTR	200		400	%

 $<sup>^{(1)}</sup>$  T<sub>amb</sub> = 25  $^{\circ}$ C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



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SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>d</sub>		3		μs
Rise time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>r</sub>		3		μs
Fall time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>f</sub>		4.7		μs
Storage time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>s</sub>		0.3		μs
Turn-on time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>on</sub>		6		μs
Turn-off time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see figure 1)	t <sub>off</sub>		5		μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t <sub>on</sub>		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t <sub>off</sub>		18		μs

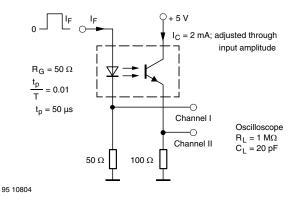


Fig. 1 - Test Circuit, Non-Saturated Operation

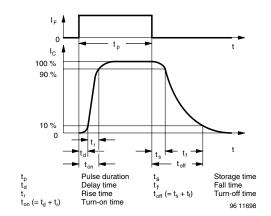


Fig. 3 - Switching Times

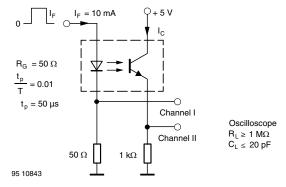


Fig. 2 - Test Circuit, Saturated Operation

## Vishay Semiconductors Optocoupler, Phototransistor Output



### **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

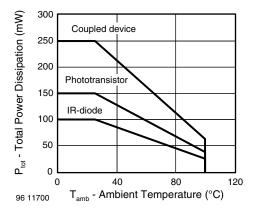


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

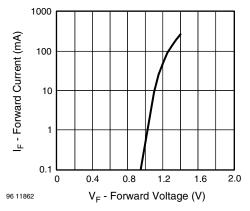


Fig. 5 - Forward Current vs. Forward Voltage

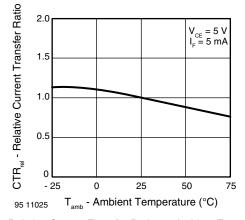


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

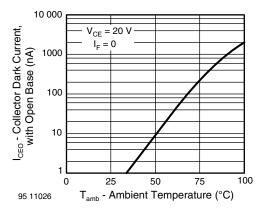


Fig. 7 - Collector Dark Current vs. Ambient Temperature

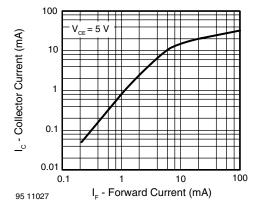


Fig. 8 - Collector Current vs. Forward Current

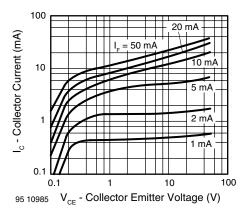


Fig. 9 - Collector Current vs. Collector Emitter Voltage



# Optocoupler, Phototransistor Output Vishay Semiconductors

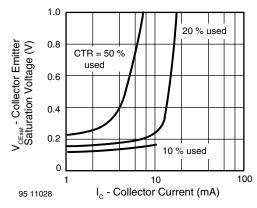


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

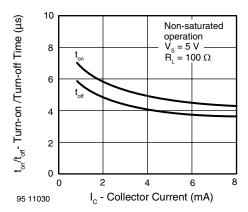


Fig. 13 - Turn-on/off Time vs. Collector Current

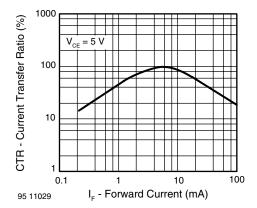


Fig. 11 - Current Transfer Ratio vs. Forward Current

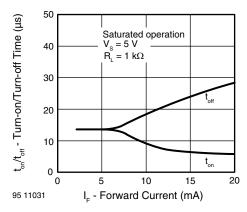
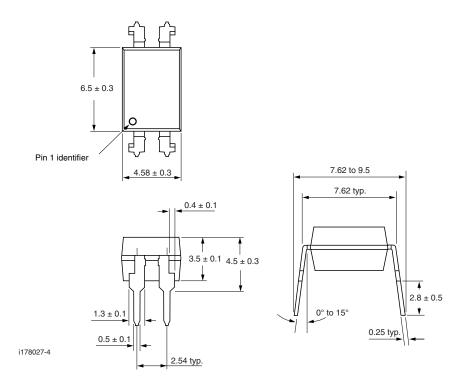


Fig. 12 - Turn-on/Turn-off Time vs. Forward Current

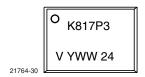
## Vishay Semiconductors Optocoupler, Phototransistor Output



### **PACKAGE DIMENSIONS** in millimeters



### **PACKAGE MARKING**





Vishay

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