

DATA SHEET

Eurologics Product Specification

LTE072T-4407

LTE072T-4408

Active matrix 7" colour

TFT LCD module

Preliminary specification

10 July, 2000



PHILIPS

CONTENTS

1.	GENERAL DESCRIPTION.....	3
2.	FEATURES.....	3
3.	APPLICATIONS.....	3
4.	QUICK REFERENCE DATA.....	4
4.1	Backlight inverter.....	4
4.2	Block diagram.....	5
5.	MECHANICAL DATA.....	6
5.1	Dimensions.....	7
5.2	Electrical connectors.....	7
6.	PINNING.....	8
6.1	Analog interface.....	8
6.2	Backlight inverter interface.....	9
7.	ELECTRICAL CHARACTERISTICS.....	10
7.1	Limiting values.....	10
7.2	Characteristics (Recommended operating conditions).....	10
7.3	Timing.....	11
7.4	Inverter characteristics.....	14
8.	OPERATING MODES.....	17
9.	OPTICAL DATA.....	18
9.1	Optical characteristics.....	18
9.2	Response time.....	19
9.3	Contrast ratio.....	19
10.	ENVIRONMENTAL DATA.....	20
10.1	Environmental tests.....	20
10.2	Mechanical tests.....	20
10.3	Electrostatic discharge (ESD).....	20
10.4	Electromagnetic compatibility (EMC).....	20
10.5	Safety.....	20
11.	Handling and safety requirements.....	21
12.	Mounting.....	21
13.	Definitions.....	21
14.	LIFE SUPPORT APPLICATIONS.....	22

Active matrix 7" colour TFT modules
Product specification

LTE072T-4407
LTE072T-4408

1. GENERAL DESCRIPTION

The LTE072T-4408 (PAL CVBS) and the LTE072T-4407 (NTSC CVBS) are TFT (thin film transistor) active matrix LCD modules. Each module comprises:

- A silicon TFT panel
- Driver electronics with control array
- An analog RGB/CVBS interface board
- An inverter
- A built-in backlight

The 7" diagonal display active area contains 480 (RGB) x 234 pixels and has a full colour capability. It has wide viewing angle and an aspect ratio of 16:9. The module can withstand intense environments.

2. FEATURES

- RGB stripe configuration
- analog RGB with horizontal and vertical sync inputs
- Video composite signal for synchronisation
- CVBS input (PAL for -4408 version, NTSC for -4407 version)
- PAL/NTSC selectable input
- Left/right, up/down control signal
- 16:9 aspect ratio
- 234 line display
- High contrast TFT LCD drive
- High speed response
- High brightness
- Wide viewing angle display
- Integrated high-efficiency backlight
- Extended temperature range
- Power supply voltage 8 to 16 V
- Fast start up backlight at low temperatures
- OSD
- Antiglare polarizer
- Built in temperature sensor

3. APPLICATIONS

- Car navigation & information
- Car entertainment
- TV, DVD and VCR monitor
- Video games
- Automation and process-control monitor

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

4. QUICK REFERENCE DATA

PARAMETER	VALUE	UNIT
Overall dimensions (without inverter)		
Width	167.0	mm
Height	102.0	mm
Depth	11.0	mm
Screen size (diagonal)	7 176	Inch mm
Active area dimensions		
Width	153.4	mm
Height	86.3	mm
Display resolution	(480 x 3) x 234	dots
Pixel dimensions		
Horizontal	0.107 x 3	mm
Vertical	0.369	mm
Pixel configuration	RGB stripe	
Supply voltage module	8 – 16	V
Supply voltage backlight inverter	8 – 16	V
Power consumption module (nominal value)	1.3	W
Power consumption backlight inverter (nominal value)	7.2	W
Advised viewing direction	12 o'clock	
Maximum contrast ratio (peak viewing angle)	> 100	
Adjustable luminance	4 - 400	Cd/m ²
Backlight lifetime	15.000	hours
Operating temperature range (see note 1)	-30 to +85	°C
Storage temperature	-40 to +90	°C
Response speed (typical at T _{amb} +22°C)	30	ms
Mass Module (with backlight inverter)	245	g

Note 1: Operating temperature range if from -30°C T_{amb} to +85°C T_{panel surface}

4.1 Backlight inverter

The backlight inverter is an integral part of the module

PARAMETER	VALUE	UNIT
Supply input voltage	8 – 16	V
Maximum input current (V _{inv} = 12 V)	1	A
Backlight dimming ratio	1 : 100	
Minimum Backlight lifetime [see note 1]	15.000	hours

Note 1: continuous operation at T_{amb} = 25°C

4.2 Block diagram

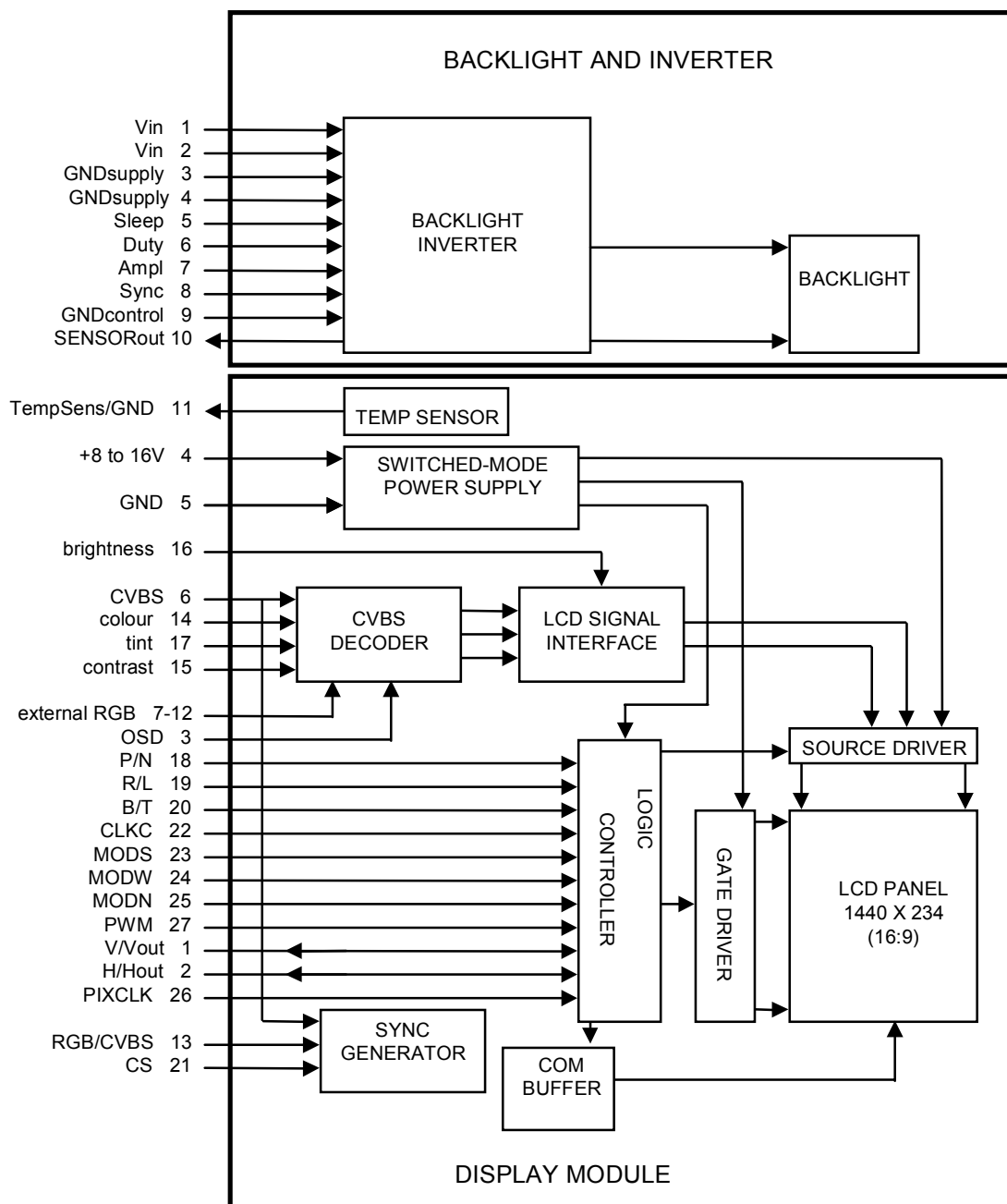


Fig.1 Block diagram.

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

5. MECHANICAL DATA

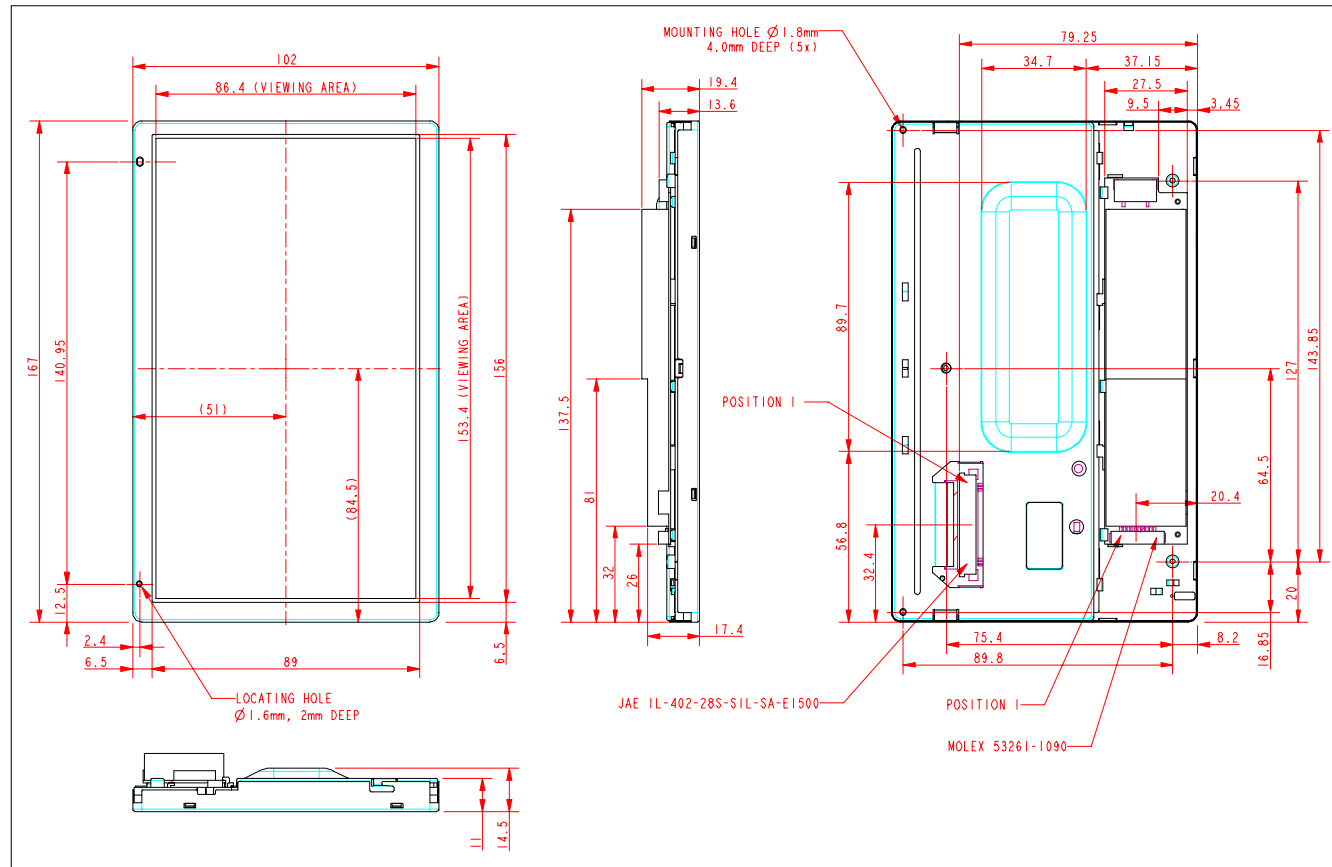


Fig.2 Mechanical Outline

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

5.1 Dimensions

PARAMETER	VALUE	UNIT
Display format	(480 x 3) x 234	dots
Active area	153.4 x 86.3	mm
Screen size (diagonal)	176 7	mm Inch
Pixel pitch horizontal vertical	3 x 0.107 0.369	mm mm
Pixel configuration	RGB stripe	
Overall dimensions (without interface board and inverter) width height depth	167.0 102.0 11	mm mm mm
Mass module with backlight inverter	245	g

5.2 Electrical connectors

SERVICE	CONNECTOR	TYPE NUMBER	NUMBER OF PINS	MATING CONNECTOR
Analog interface	JAE; see note 1	JAE-IL-402-28S-SIL-SA-E1500	28	Flex Foil, pitch = 1.0 mm
Inverter	Molex	53261-1090	10	Molex 51021-1000

Notes

- Bottom contact entry

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

6. PINNING

6.1 Analog interface

SYMBOL	PIN	I/O	DESCRIPTION
V/V _{out}	1	I/O	vertical sync pulse I/O; negative going pulses for both input and output
H/H _{out}	2	I/O	horizontal sync pulse I/O; negative going pulses for both input and output
OSD	3	I	On screen data select input; default LOW for OSD OFF
V _{sup}	4	I	Supply voltage (+8 to 16 V)
GND	5	-	Ground
CVBS	6	I	Composite video input; module can synchronize on CVBS or CS input
GND	7	-	Ground
R	8	I	Red video input
GND	9	-	Ground
G	10	I	Green video input
T _{sensor} /GND	11	-	Temperature sensor / AC ground
B	12	I	Blue video input
RGB/CVBS	13	I	RGB/CVBS select input; default LOW (10 kohm pull-down resistor) selects CVBS; RGB/CVBS=HIGH (+5V) selects RGB (CS is then sync input)
Colour	14	I	0 to 3.5 V colour control input (CVBS only)
Contrast	15	I	0 to 3.5 V contrast control input (CVBS only)
Brightness	16	I	0 to 3.5 V brightness control input
Tint	17	I	0 to 3.5 V Tint control input (CVBS-NTSC only)
P/N	18	I	PAL/NTSC mode select; default HIGH (10 kohm pull-up resistor) selects NTSC 60 Hz mode. For PAL mode 50 Hz versions, P/N is default LOW; see note 3
R/L	19	I	scan direction right/ left; default HIGH (10 kohm pull-up resistor) for left-to-right scanning; R/L= LOW (0 V) gives right-to-left scanning
B/T	20	I	scan direction bottom-to-top/ top-to-bottom; default HIGH (10 kohm pull up) for top-to-bottom scanning; B/T= LOW (0 V) gives bottom-to-top scanning
CS	21	I	composite sync input; positive or negative-going TTL signal
CLKC	22	I	clock select (internal sync/ external clock); see note 1
MODS	23	I	Aspect ratio control input
MODW	24	I	Aspect ratio control input
MODN	25	I	Aspect ratio control input
PIXCLK	26	I	pixel clock
PWM	27	O	backlight synchronization output; see note 2
n.c.	28	-	Not used. Do not connect.

Notes

- For internal clock mode CLKC input is default HIGH (10 Kohm pull up), and PIXCLK will be continuously low. With CLKC=LOW, PIXCLK is input as the external clock signal, V and H become sync inputs and P/N,MODS,MODW and MODN must be set HIGH
- PWM signal can be used to control the PWM dimming frequency directly. It can also be used to control dimming in combination with HSY. The PWM signal should be used only with standard NTSC or PAL signal inputs.
- Only select when module is not operating

Active matrix 7" colour TFT modules
Product specification

LTE072T-4407
LTE072T-4408

6.2 Backlight inverter interface

Pin No.	Symbol	Description	Remarks
CN1-1	V_{in}	Supply voltage	8.0 ~ 16.0 V
CN1-2	V_{in}	Supply voltage	8.0 ~ 16.0 V
CN1-3	GND_{supply}	Supply ground	
CN1-4	GND_{supply}	Supply ground	
CN1-5	Sleep	Sleep control by digital voltage	0.0 ~ 5.0 V
CN1-6	Duty	Duty cycle dimming control by analog or PWM voltage	0.0 ~ 5.0 V
CN1-7	Ampl	Amplitude control by analog or PWM voltage	0.0 ~ 5.0 V
CN1-8	Sync	Synchronization of internal PWM	0.0 ~ 5.0 V
CN1-9	$GND_{control}$	Control ground	
CN1-10	$Sensor_{out}$	Sensor readout	10 k Ω NTC

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

7. ELECTRICAL CHARACTERISTICS

7.1 Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). GND = 0 volt and $T_{amb} = 25^{\circ}\text{C}$ unless otherwise stated.

SYMBOL	DESCRIPTION	MIN.	MAX.	UNIT
V_{sup}, V_{inv}	power supply input voltage	0	20	V
RH	relative humidity; see note 1	-	90	%
T_{stg}	storage temperature; see note 1	-40	+90	$^{\circ}\text{C}$
T_{oper}	operation temperature; see note 2	-30	+85	$^{\circ}\text{C}$

Notes

1. No condensation is allowed under any condition.
2. Operating temperature range if from -30°C T_{amb} to $+85^{\circ}\text{C}$ $T_{panel\ surface}$.

7.2 Characteristics (Recommended operating conditions)

SYMBOL	DESCRIPTION	CONDITION	MIN.	TYP.	MAX.	UNIT
Power supply						
V_{sup}	Supply voltage (pin 4)	Within 300 ms	8.0	12.0	16.0	V
$V_{sup, ripple}$	Ripple on supply voltage	Frequency > 400 Hz			0.1	V
I_{sup}	Supply current	8 – 16 V	90	110	250	mA
Logic levels (V/Vout, H/Hout, OSD, RGB/CVBS, P/N, R/L, B/T, CS, CLKC, MODS, MODW, MODN, PIXCLK, PWM)						
V_{IH}	High-level input voltage		3.5	5.0	5.2	V
I_{IL}	Low-level input voltage		0	-	0.8	V
Analog inputs						
$V_{RGB(p-p)}$	Analog video input voltage (peak-to-peak value)	$R_i = 75\ \Omega$	0	0.7	1.1	V
External control inputs						
Brightness	Brightness control		0	2.0	3.5	V
Contrast	Contrast control		0	2.8	3.5	V
Tint	Tint control		0	3.0	3.5	V
Colour	Colour control		0	2.8	3.5	V
Backlight inverter						
V_{inv}	Supply voltage	See note 1	8.0	12.0	16.0	V
I_{inv}	Supply current	$V_{inv} = 12.0\ \text{V}$		600		mA
Sleep	Sleep control voltage		-0.3		$V_{in}+0.3$	V
Duty	Dimming control voltage		-0.3		$V_{in}+0.3$	V
Ampl	Amplitude control voltage		-0.3		$V_{in}+0.3$	V
Sync	Synchronisation voltage		-0.3		$V_{in}+0.3$	V

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407

LTE072T-4408

Note 1 Inverter does not meet the full dimming range, lightoutput range and boost current range at supply voltages < 11V. Start-up of backlight at a supply voltage < 9 volt is not 100% guaranteed.

7.3 Timing

7.3.1 EXTERNAL CLOCK MODE (CHANGE CLOCK CLKC = LOW)

- Module is synchronised to horizontal sync output pulses HSY and VSY
- Input clock CLK, HSY and VSY are switched to input mode
- Module is activated by CLK (CLK timing is with respect to horizontal pixel sampling clock PIXCLK)

Timing: external clock mode

SYMBOL	DESCRIPTION	MIN.	TYP.	MAX.	UNIT
$t_{W(VI)}$	Vertical sync pulse width	1H	3H	5H	
t_{H01}	Vertical sync hold time	1.0	-	-	μ s
t_{SU1}	Vertical sync set-up time	1.0	-	-	μ s
$t_{W(HI)}$	Horizontal sync pulse width	1.0	5.0	9.0	μ s
t_{H02}	Horizontal sync hold time	25	-	-	ns
t_{SU2}	Horizontal sync set-up time	25	-	-	ns
f_{clk}	CLK frequency	9.0	10.0	11.0	MHz
f_{VSY}, f_{Vin}	VSY, V_{in} frequency	50	$f_{HSY} / 312$	$f_{HSY} / 300$	Hz
f_{HSY}, f_{Hin}	HSY, H_{in} frequency	$f_{clk} / 650$	$F_{clk} / 610$	$f_{clk} / 570$	Hz

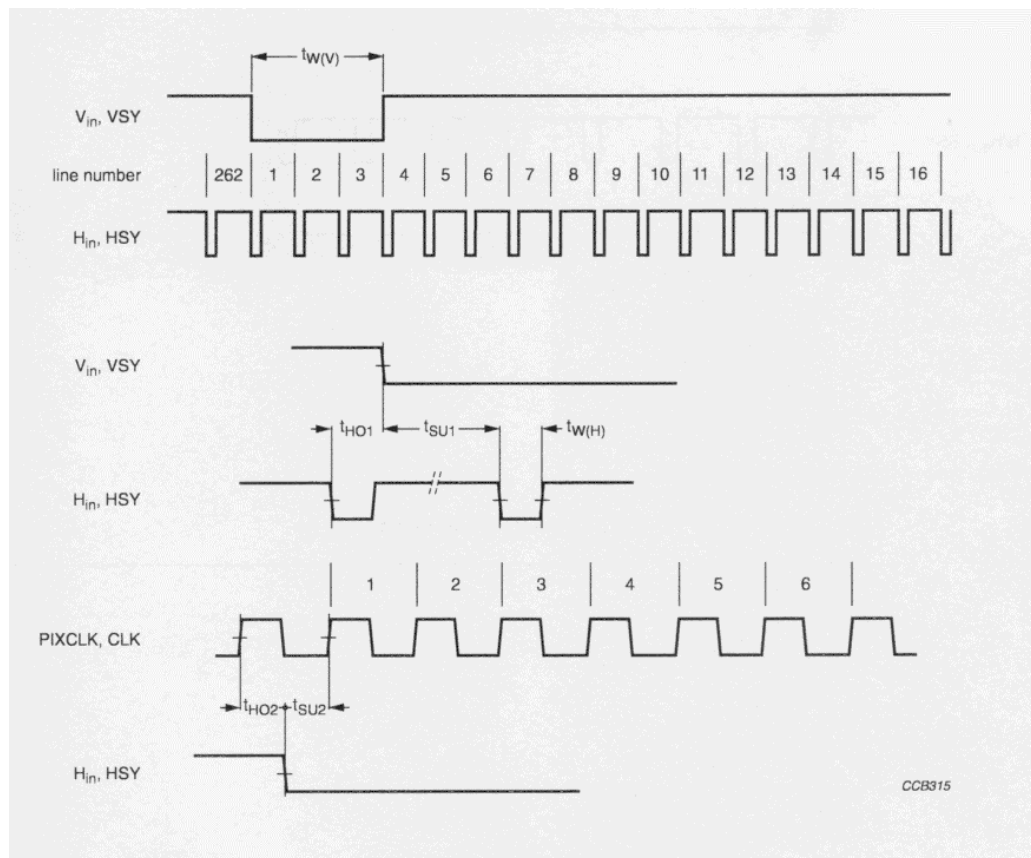
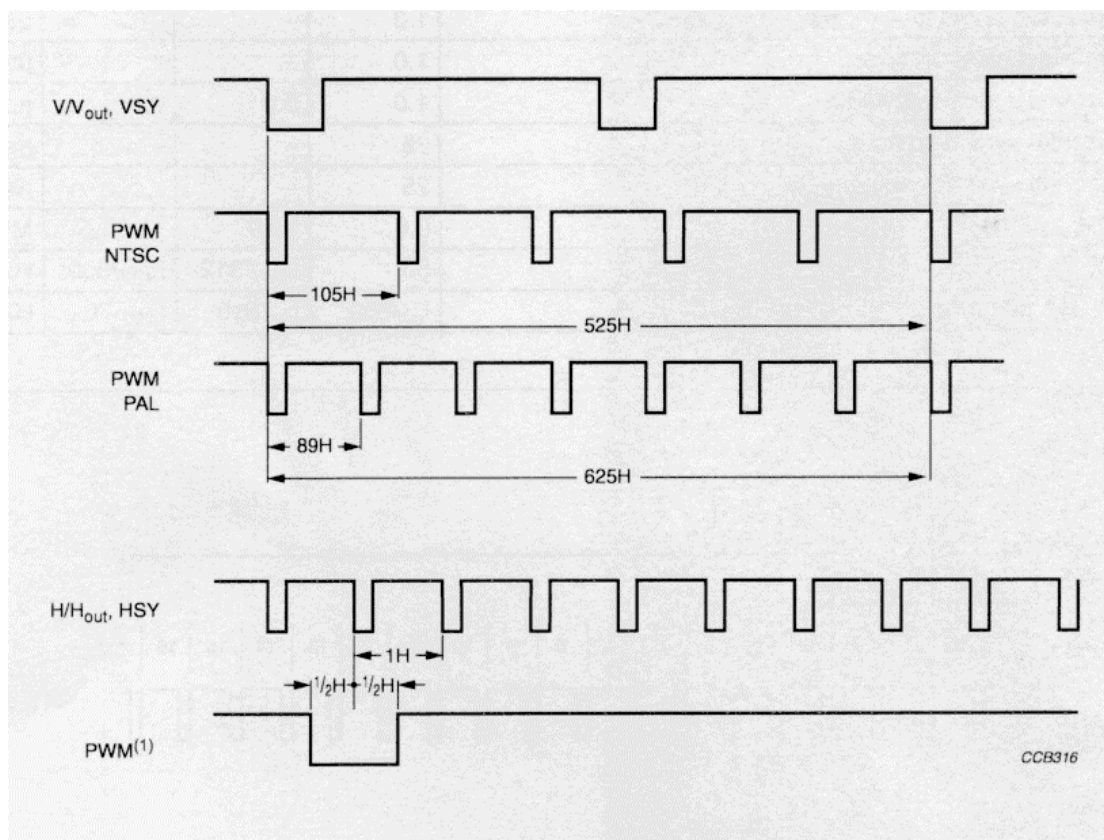


Fig. 3 Horizontal and vertical timing in external clock mode

7.3.2 PWM SIGNAL



(1) Typical PWM frequency, PAL: 175 Hz; NTSC: 150 Hz

Fig. 4 Output timing, PWM signal

7.4 Inverter characteristics

7.4.1 DIMMING FUNCTION

Dimming can be controlled in two ways (but not simultaneously):

- By applying an analog control voltage on CN1-6.
- By applying a digital pulse width modulated control signal on CN1-6.

7.4.1.1 Applying a dc control voltage

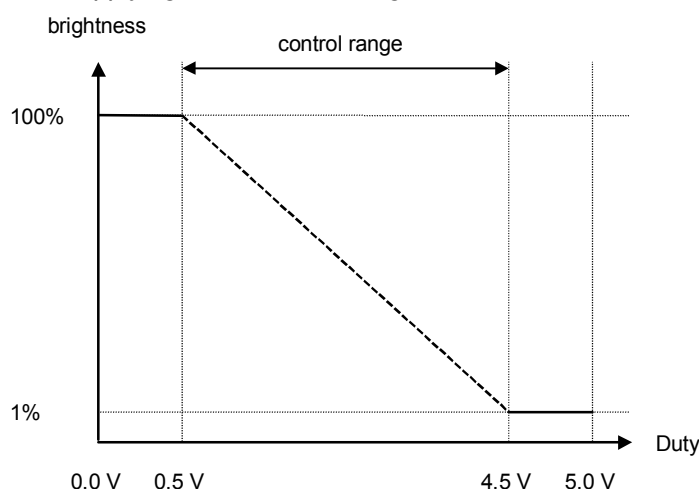


Figure 1 - brightness as a function of a dc voltage on CN1-6

Brightness is controlled by an analog control voltage. Below $0.5V \pm 7\%$ and above $4.5V \pm 7\%$ brightness control is rejected.

7.4.1.2 Applying a pulse width modulated control signal

Brightness is determined by the dc-level of a pulse width modulated control signal Duty. The upper and lower voltage limits, as well as the duty cycle determine the brightness. Internally a low pass filter is applied. The control frequency should be 450 Hz or higher.

7.4.1.3 Interfacing characteristics CN1-6

Item	Unit	Min	Typ	Max
absolute maximum input level	V	-0.5		21
recommended input level	V	0		5
control range lowest voltage	V	0.46	0.50	0.54
control range highest voltage	V	4.2	4.5	4.8
input impedance	k Ω	475	500	525
	pF	23	25	27
termination voltage	V		1.4	
PWM control logic input level	V	0		5
PWM control slew rate	ns/V	5		500
PWM control frequency range	Hz	400		4000

7.4.1.4 brightness response to duty control

Item	Unit	Min	Typ	Max
------	------	-----	-----	-----

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

panel brightness at no dimming	Cd/m ²	350	400	
panel brightness at max dimming	Cd/m ²	3.5	4	

Note: $T_{\text{ambient}} = 25^{\circ}\text{C}$, $V_{\text{ampl}} \approx 1.65\text{V}$.

7.4.2 AMPLITUDE CONTROL

Amplitude of the lamp current can be controlled in two ways (but not simultaneously):

- By applying an analog control voltage on CN1-7.
- By applying a digital pulse width modulated control signal on CN1-7.

An external temperature sensor (Mitsubishi thermistor RH16-3H103FB, 10k Ω @ 25 $^{\circ}\text{C}$, $B_{25-85} = 3486\text{K}$), connected to CN3, is used to monitor the working point of the CCFT. Control logic is not on the inverter but external. The sensor wires are lead to CN1 directly without any interfacing. Boost mode should only be used for a short period of time at low ambient temperature.

With the maximum amplitude (boost mode) and no dimming the following typical run-up times can be achieved:

Ambient temperature (still air) [$^{\circ}\text{C}$]	Typical time to reach 200 cd/m ² at the specified temperature [s]
-30	200
-20	100
-10	60

Note: $V_{\text{in}} = 12\text{V}$

7.4.2.1 Applying a dc control voltage

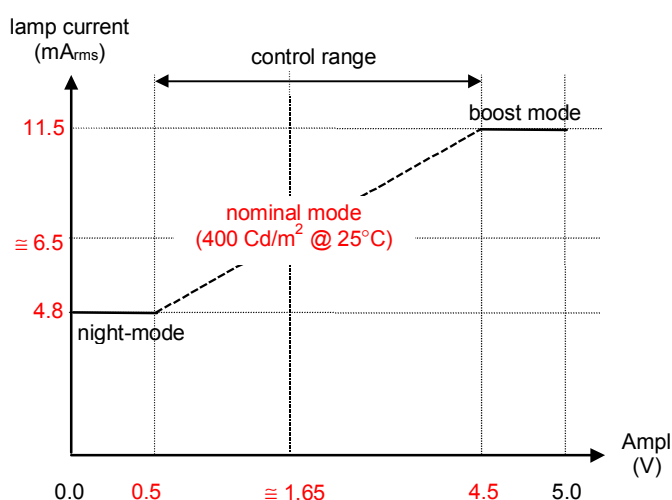


Figure 2 - amplitude as function of a dc voltage on CN1-7

Amplitude is controlled by an analog control voltage. Below 0.5V $\pm 7\%$ and above 4.5V $\pm 7\%$ amplitude control is rejected. Between 0.5V and 4.5V lamp current is almost linear with the control voltage.

7.4.2.2 Applying a pulse width modulated control signal

Amplitude is determined by the dc-level of a pulse width modulated control signal Ampl. The upper and lower voltage limits, as well as the duty cycle determine the amplitude. Internally a low pass filter is applied. The control frequency should be 400 Hz or higher.

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

7.4.2.3 Interfacing characteristics CN1-7

Item	Unit	Min	Typ	Max
absolute maximum input level	V	-0.5		21
recommended input level	V	0		5
control range lowest voltage	V	0.46	0.50	0.54
control range highest voltage	V	4.2	4.5	4.8
input impedance	k Ω	475	500	525
	pF	23	25	27
termination voltage	V		1.37	
PWM control logic input level	V	0		5
PWM control slew rate	ns/V	5		500
PWM control frequency range	Hz	400		4000

7.4.2.4 lamp current response to amplitude control

Item	Unit	Min	Typ	Max
night mode current ($V_{\text{ampl}} = 0\text{V}$)	mA _{rms}	4.4	4.8	5.2
nominal mode current ($V_{\text{ampl}} \approx 1.65\text{V}$)	mA _{rms}	6.0	6.5	7.0
boost mode current ($V_{\text{ampl}} = 5\text{V}$)	mA _{rms}	10.7	11.5	12.3
linearity	%			tbd

Note: $T_{\text{ambient}} = 25^{\circ}\text{C}$, $V_{\text{duty}} = 0\text{V}$.

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

8. OPERATING MODES

MODE	DESCRIPTION		SOURCE ASPECT	ADDRESS		
				MODS	MODW	MODN
NTSC						
1	Full display mode		4:3	H	H	H
2	Wide-1 mode		4:3	H	H	L
3	normal mode		4:3	H	L	H
6	14:9 or overscan mode		4:3	L	H	L
7	Normal right mode		4:3	L	L	H
8	Normal left mode		4:3	L	L	L

MODE	DESCRIPTION		SOURCE ASPECT	ADDRESS		
				MODS	MODW	MODN
PAL						
1	Full display mode		4:3	H	H	H
2	Wide-1 mode		4:3	H	H	L
3	normal mode		4:3	H	L	H
4	Cinema mode		16:9	H	L	L
5	Wide-2 mode		4:3	L	H	H
6	14:9 or overscan mode		4:3	L	H	L
7	Normal right mode		4:3	L	L	H
8	Normal left mode		4:3	L	L	L

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

9. OPTICAL DATA

9.1 Optical characteristics

$T_{amb} = +22 \pm 3^{\circ}\text{C}$; supply voltage module and inverter is 12V; elapsed time from switch-on is greater than 30 minutes; RGB test patterns only; factory presets used; dimming is not applied; measurements are made perpendicular to the panel; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
L	Luminance (note 1)	Peak white	350	400		Cd/m ²
CR	Contrast Ratio	At α_{opt}	100:1	200:1	-	
α	Viewing angle: $\theta = 180^{\circ}$ (9 o'clock) $\theta = 0^{\circ}$ (3 o'clock) $\theta = 90^{\circ}$ (12 o'clock) $\theta = 270^{\circ}$ (6 o'clock) $\theta = 180^{\circ}$ (9 o'clock) $\theta = 0^{\circ}$ (3 o'clock) $\theta = 90^{\circ}$ (12 o'clock) $\theta = 270^{\circ}$ (6 o'clock)	CR ≥ 5 CR ≥ 10	55 55 40 40 45 45 30 30	65 65 50 50 55 55 40 40	- - - - - - - -	Deg Deg Deg Deg Deg Deg Deg Deg
t_{res}	rise time (t_r) [see 9.2] fall time (t_f) [see 9.2]			45 15		ms ms
X_R Y_R X_G Y_G X_B Y_B X_W Y_W	Colour coordinates: Red Red Green Green Blue Blue White White	Peak white Peak white	0.545 0.275 0.257 0.526 0.122 0.066 0.255 0.282	0.595 0.325 0.307 0.576 0.147 0.116 0.307 0.332	0.645 0.375 0.357 0.626 0.172 0.166 0.357 0.382	
α_{opt}	Optimum contrast angle	6 o'clock	-	1	-	Deg

Note 1: Nominal lamp current (6.5 mArms)

9.2 Response time

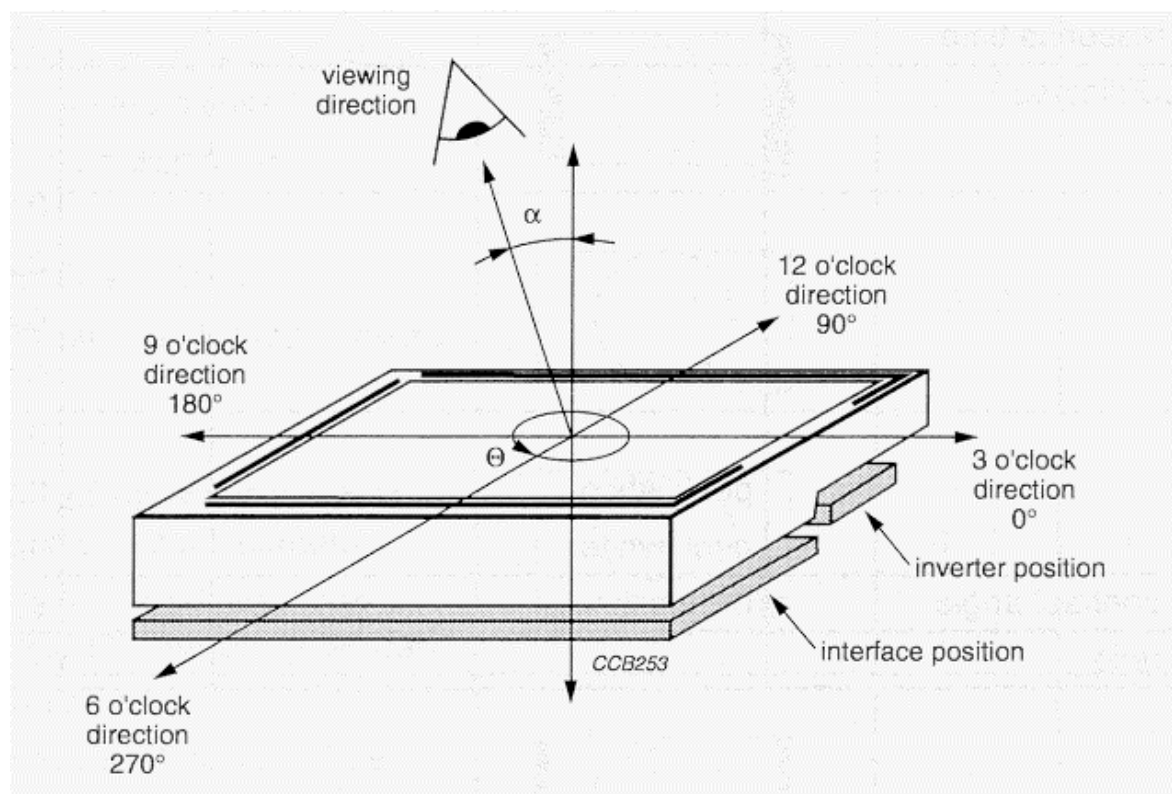
Rise time is the time for luminance to change from 10% to 90% as a result of a change of electrical condition, fall time is the time for luminance to change from 90% to 10% as a result of a change of electrical condition.

The typical rise and fall time are defined at 22°C.

9.3 Contrast ratio

The contrast ratio (CR) is the ratio between the transmission (τ) in a full white area ($R=G=B=1$) and the transmission (τ_d) in a dark area ($R=G=B=0$):

$$CR = \tau / \tau_d$$



α = declination
 θ = azimuth

Fig. 7 Viewing angle

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

10. ENVIRONMENTAL DATA

10.1 Environmental tests

TEST	CONDITIONS	METHOD	REMARK
High temperature operating test	T _{panel} = +85°C for 240 hrs	IEC 60 068-2-2Bp	panel surface temperature
Low temperature operating test	T _{amb} = -30°C for 240 hrs	IEC 60 068-2-2Ab	
High temperature storage test	T _{amb} = +90°C for 240 hrs	IEC 60 068-2-2Bp	module not operational
Low temperature storage test	T _{amb} = -40°C for 240 hrs	IEC 60 068-2-1Ab	module not operational
High temperature, high humidity operating test	T _{amb} = +60°C, RH = 90% for 240 hrs; no condensation	IEC 60 068-2-3CA	
Thermal shock	10 cycles of T _{amb} = -30°C to T _{amb} = +85°C	IEC 60 068-2-14Na	module not operational
UV exposure resistance	765 W/m ² for 168 hrs	IEC 60 068-2-5Sa	module not operational

10.2 Mechanical tests

TEST	CONDITIONS	METHOD	REMARK
Shock test	3 directions: X, Y, Z axes; 6 repeats; peak acceleration = 100 G; pulse duration = 6 ms ½ sine wave	IEC 60 068-2-27Ea	Not operated; not packed
Vibration test	3 directions: X, Y and Z axes; 6 repeats; sweep time = 11 minutes; peak acceleration = 10 G; frequency = 10 to 150 Hz; amplitude = 1.5 mm peak-to-peak sine wave	IEC 60 068-2-6Fc	Not operated; not packed

10.3 Electrostatic discharge (ESD)

Under directive "89/336/EEG" conforms with "EN50082-1".

10.4 Electromagnetic compatibility (EMC)

Complies with "FCC part 15".

Under directive "89/336/EEG" conforms with "EN55022/B" and "EN61000-4-6".

10.5 Safety

Complies with "IEC 60950".

Active matrix 7" colour TFT modules

Product specification

LTE072T-4407
LTE072T-4408

11. HANDLING AND SAFETY REQUIREMENTS

WARNING	
The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the liquid crystalline material. In case of contamination with liquid crystal material, wash immediately with water and soap.	
The display module contains parts that operate at high voltage. Under no circumstances should the front or back cover or inverter shielding be removed during operation. Do not touch circuit at backside of module.	

CAUTION	
At temperatures lower than the rated storage temperature, the liquid crystal solidifies causing permanent damage to the display.	
At temperatures higher than the rated storage temperature, the liquid crystal turns into an isotropic liquid and may not recover.	
The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronics components.	
Disassembling the display module can cause permanent damage and invalidates the warranty agreements.	
Observe general precautions that are common to handling delicate electronic components. The glass can break and polarizers can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.	

12. MOUNTING

CAUTION	
Allow enough space at the back of the module for sufficient airflow to disperse heat generated by the backlighting system.	
Recommended tapping (T2) screw diameter for mounting of the module is 2.2mm. Maximum torque is 1.3 kgf.cm.	

13. DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the	

Active matrix 7" colour TFT modules

LTE072T-4407

Product specification

LTE072T-4408

Data sheet status

specification.

14. LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Flat Display Systems customers using or selling these products for use in such applications do so at their own risk and agree to fully Philips Flat Display Systems for any damages resulting from such improper use or sale.