General Purpose Transistors

NPN Silicon

Features

- Moisture Sensitivity Level: 1
- ESD Rating Human Body Model: >4000 V – Machine Model: >400 V
- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}		Vdc
BC846, SBC846		65	
BC847, BC850, SBC847		45	
BC848, BC849, SBC848		30	
Collector-Base Voltage	V _{CBO}		Vdc
BC846, SBC846		80	
BC847, BC850, SBC847		50	
BC848, BC849, SBC848		30	
Emitter-Base Voltage	V _{EBO}		Vdc
BC846, SBC846		6.0	
BC847, BC850, SBC847		6.0	
BC848, BC849, SBC848		5.0	
Collector Current – Continuous	Ι _C	100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C	P _D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) $T_A = 25^{\circ}C$	P _D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	R_{\thetaJA}	417	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C

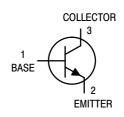
1. FR–5 = 1.0 \times 0.75 \times 0.062 in.

2. Alumina = 0.4 \times 0.3 \times 0.024 in 99.5% alumina.



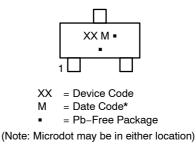
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MARKING DIAGRAM



*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

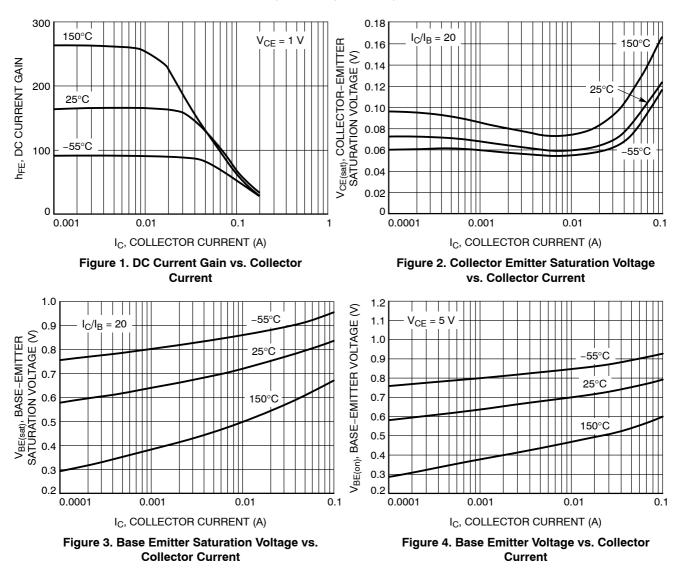
See detailed ordering and shipping information in the package dimensions section on page 12 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

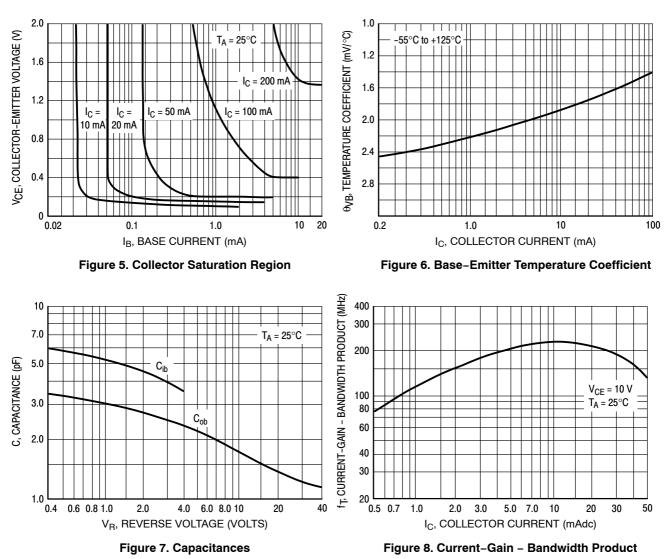
Characteristic			Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage BC846A, B, SBC846A, B(I _C = 10 mA)BC847A, B, C, BC850B, C, SBC847CBC848A, B, C, BC849B, C, SBC848B		V _{(BR)CEO}	65 45 30	- - -	- - -	V
Collector – Emitter Breakdown Voltag (I _C = 10 μA, V _{EB} = 0)	V _{(BR)CES}	80 50 30	- -	- - -	V	
Collector – Base Breakdown Voltage ($I_C = 10 \ \mu A$)	BC846A, B, SBC846A, B BC847A, B, C, BC850B, C, SBC847C BC848A, B, C, BC849B, C, SBC848B	V _{(BR)CBO}	80 50 30	- - -	- - -	V
Emitter – Base Breakdown Voltage (I _E = 1.0 μA)	BC846A, B, SBC846A, B BC847A, B, C, BC850B, C, SBC847C BC848A, B, C, BC849B, C, SBC848B	V _{(BR)EBO}	6.0 6.0 5.0	- - -	- - -	V
Collector Cutoff Current (V _{CB} = 30 V) $(V_{CB} = 30 \text{ V}, \text{ T}_{\text{A}} = 150^{\circ}\text{C})$					15 5.0	nA μA
ON CHARACTERISTICS						
(I _C = 2.0 mA, V _{CE} = 5.0 V) BC846	BC846A, BC847A, BC848A, SBC846A BC847B, BC848B, SBC846B, SBC848B BC847C, BC848C, SBC847C A, BC847A, BC848A, SBC846A, SBC846A BC846B, BC847B, BC848B, BC849B, BC850B, SBC846B, SBC848B CBC946C, BC947C	h _{FE}	- - 110 200 420	90 150 270 180 290 520	- - 220 450 800	_
$\label{eq:BC847C, BC848C, BC849C, BC850C, SBC847C} BC847C, BC849C, BC849C, BC849C, BC847C \\ Collector – Emitter Saturation Voltage (I_C = 10 mA, I_B = 0.5 mA) \\ (I_C = 100 mA, I_B = 5.0 mA) \\ \end{array}$		V _{CE(sat)}			0.25 0.6	V
Base – Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)			-	0.7 0.9	-	V
Base – Emitter Voltage (I _C = 2.0 mA, V _{CE} = 5.0 V) (I _C = 10 mA, V _{CE} = 5.0 V)			580 -	660 -	700 770	mV

SMALL-SIGNAL CHARACTERISTICS

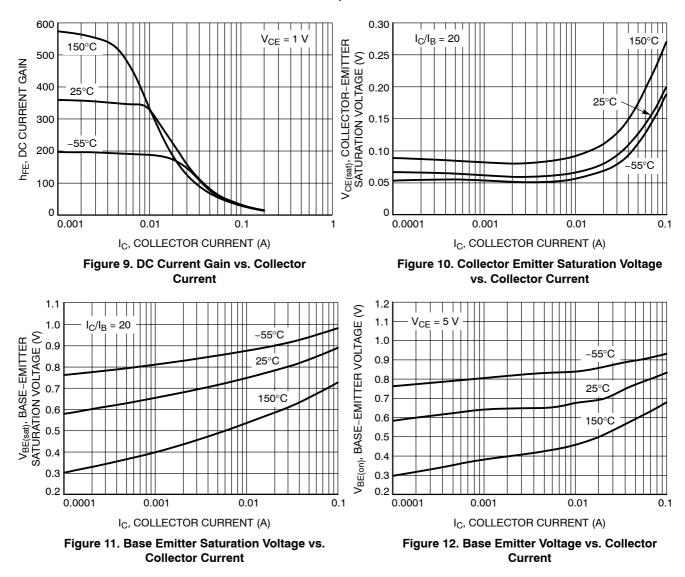
Current – Gain – Bandwidth Product ($I_c = 10 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)			100	-	-	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)			-	-	4.5	pF
Noise Figure (I _C = 0.2 mA,	NF				dB	
$V_{CE} = 5.0 \text{ Vdc}, R_S = 2.0 \text{ k}\Omega,$ BC846A,B, BC847A,B,C, BC848A,B,C,			-	-	10	
SBC846A, B, SBC847C, SBC848B			-	-	4.0	
f = 1.0 kHz, BW = 200 Hz)	BC849B,C, BC850B,C					



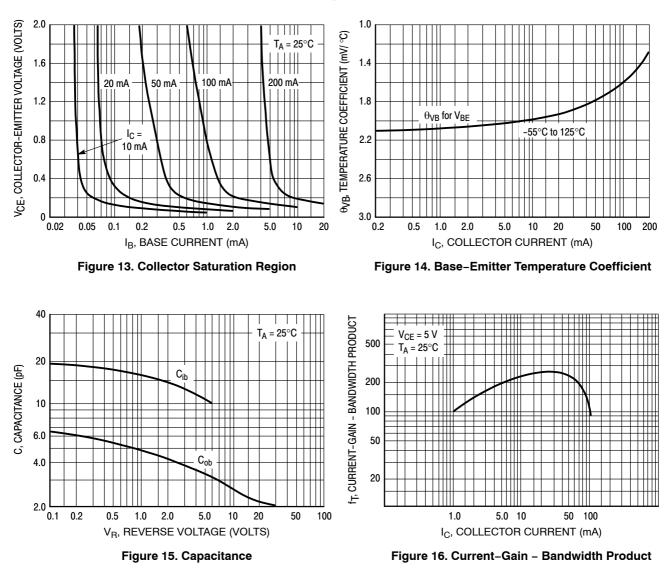
BC846A, BC847A, BC848A, SBC846A



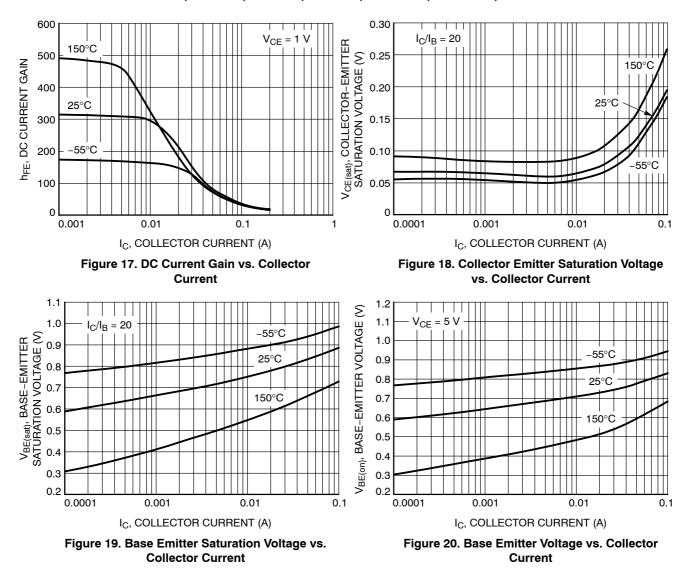
BC846A, BC847A, BC848A, SBC846A



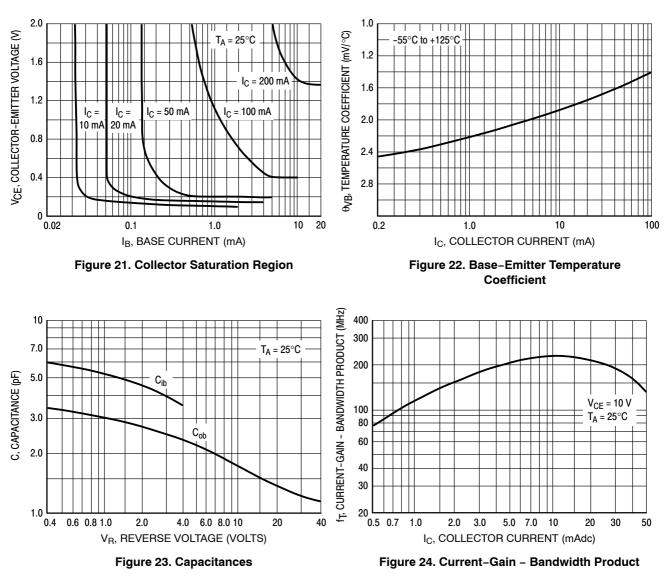
BC846B, SBC846B



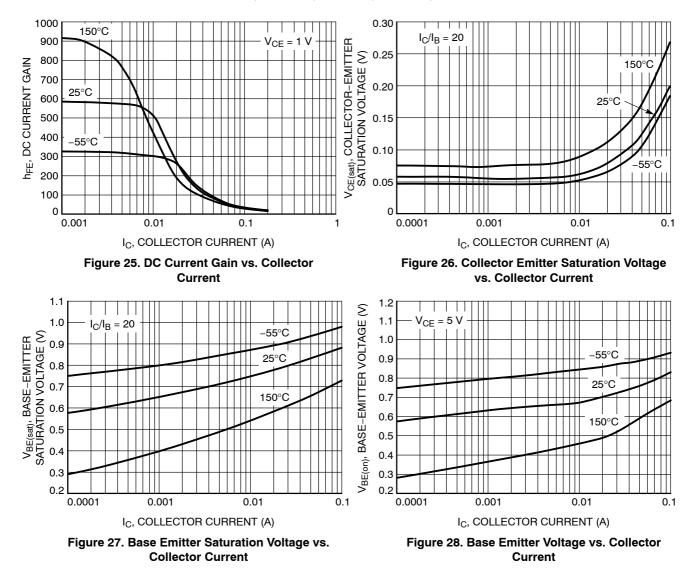
BC846B, SBC846B



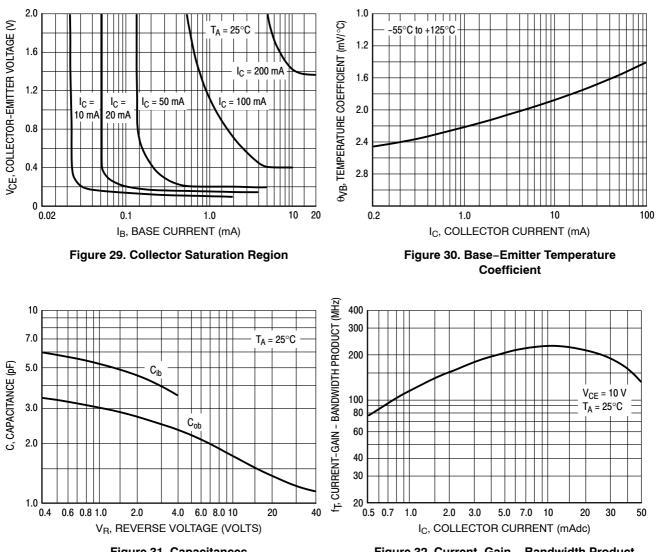
BC847B, BC848B, BC849B, BC850B, SBC846B, SBC847B, SBC848B



BC847B, BC848B, BC849B, BC850B, SBC846B, SBC847B, SBC848B



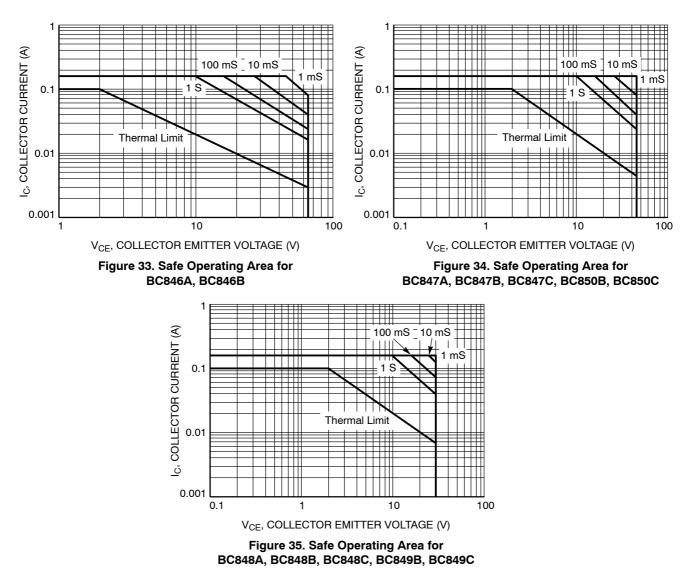
BC847C, BC848C, BC849C, BC850C, SBC847C



BC847C, BC848C, BC849C, BC850C, SBC847C

Figure 31. Capacitances

Figure 32. Current–Gain – Bandwidth Product



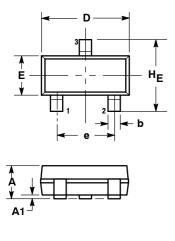
ORDERING INFORMATION

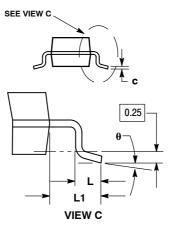
Device	Marking	Package	Shipping [†]		
BC846ALT1G		SOT-23			
SBC846ALT1G	1A	(Pb-Free)	3,000 / Tape & Reel		
BC846ALT3G	1A	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC846BLT1G	10	SOT-23			
SBC846BLT1G	1B	(Pb-Free)	3,000 / Tape & Reel		
BC846BLT3G	10	SOT-23	10.000 (Too & Dool		
SBC846BLT3G		(Pb-Free)	10,000 / Tape & Reel		
BC847ALT1G		SOT-23 (Pb-Free)	3,000 / Tape & Reel		
BC847ALT3G	1E	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC847BLT1G	15	SOT-23			
SBC847BLT1G	1F	(Pb-Free)	3,000 / Tape & Reel		
BC847BLT3G	1F	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC847CLT1G	10	SOT-23			
SBC847CLT1G	1G	(Pb-Free)	3,000 / Tape & Reel		
BC847CLT3G	1G	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC848ALT1G	1J	SOT-23 (Pb-Free)	3,000 / Tape & Reel		
BC848BLT1G		SOT-23			
SBC848BLT1G	1K	(Pb-Free)	3,000 / Tape & Reel		
BC848BLT3G	1K	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC848CLT1G		SOT-23 (Pb-Free)	3,000 / Tape & Reel		
BC848CLT3G	1L	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC849BLT1G		SOT-23 (Pb-Free)	3,000 / Tape & Reel		
BC849BLT3G	2B	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC849CLT1G		SOT-23 (Pb-Free)	3,000 / Tape & Reel		
BC849CLT3G	2C	SOT-23 (Pb-Free)	10,000 / Tape & Reel		
BC850BLT1G	2F	SOT-23 (Pb-Free)			
BC850CLT1G	2G	SOT-23 (Pb-Free)	- 3,000 / Tape & Reel		

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AP





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH

- CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL. 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS. OR GATE BURRS.

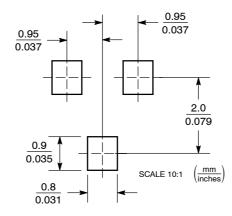
	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.040	0.044	
A1	0.01	0.06	0.10	0.001	0.002	0.004	
b	0.37	0.44	0.50	0.015	0.018	0.020	
c	0.09	0.13	0.18	0.003	0.005	0.007	
D	2.80	2.90	3.04	0.110	0.114	0.120	
Е	1.20	1.30	1.40	0.047	0.051	0.055	
е	1.78	1.90	2.04	0.070	0.075	0.081	
L	0.10	0.20	0.30	0.004	0.008	0.012	
L1	0.35	0.54	0.69	0.014	0.021	0.029	
HE	2.10	2.40	2.64	0.083	0.094	0.104	
θ	0°		10°	0°		10°	

STYLE 6:

PIN 1. BASE 2. EMITTER

3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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