

100mA / 50V Digital transistors (with built-in resistors)

DTC144EB / DTC144EM / DTC144EE / DTC144EUA / DTC144EKA

Applications

Inverter, Interface, Driver

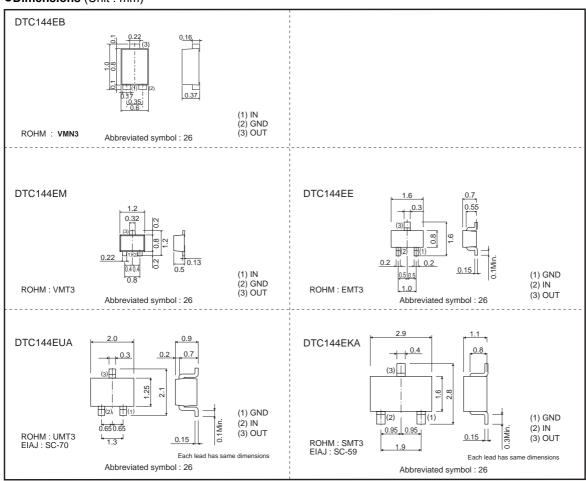
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

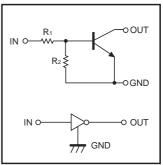
●Dimensions (Unit: mm)



Packaging specifications

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	Package	VMN3	VMT3	EMT3	UMT3	SMT3
Part No.	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	T2L TL		T106	T146
	Basic ordering unit (pieces)	8000	8000	3000	3000	3000
DTC144EB		0	_	_	-	_
DTC144EM		-	0	-	-	_
DTC144EE		-	-	0	-	_
DTC144EU	4	-	-	-	0	_
DTC144EK	A	-	-	-	-	0

●Equivalent circuit



R₁=R₂=47kΩ

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits					Unit
Parameter		DTC144EB	DTC144EM	DTC144EE	DTC144EUA	DTC144EKA	
Supply voltage	Vcc	50				V	
Input voltage	Vin	-10 to +40					V
Outrout summent	lo	30					mA
Output current	IC(Max.)	100					
Power dissipation	ower dissipation P _D 150				20	00	mW
Junction temperature	unction temperature Tj 150			°C			
Storage temperature	Tstg	-55 to +150					°C

●Electrical characteristics (Ta=25°C)

	•		•			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lanut valtage	VI(off)	-	-	0.5	V	Vcc=5V, Io=100μA
Input voltage	VI(on)	3	_	_	V	Vo=0.3V, Io=2mA
Output voltage	Vo(on)	-	0.1	0.3	V	Io/I=10mA/0.5mA
Input current	lı	-	-	0.18	mA	Vi=5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V
DC current gain	Gı	68	-	-	-	Vo=5V, Io=5mA
Input resistance	R ₁	32.9	47	61.1	kΩ	-
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	-	_
Transition frequency	f⊤ *	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz

^{*} Characteristics of built-in transistor

•Electrical characteristic curves

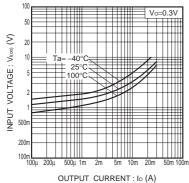


Fig.1 Input voltage vs. output current (ON characteristics)

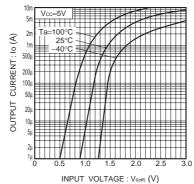


Fig.2 Output current vs. input voltage (OFF characteristics)

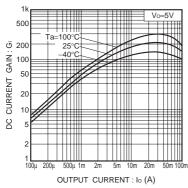


Fig.3 DC current gain vs. output current

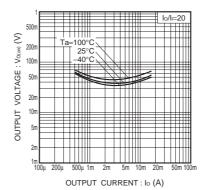


Fig.4 Output voltage vs. output current

Notes

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