Pin # ADAF  1 PD2/N 2 PD3/N 3 PD4/S 4 PD5/S	MISO		1 CONNECTOR S		Rev. 2 (4 Mar 09)
1 PD2/N 2 PD3/N 3 PD4/S	MISO				
1 PD2/N 2 PD3/N 3 PD4/S	MISO	APAI IIILVUU		MICROCOPE-11	MICROSTAMP11
2 PD3/N 3 PD4/S			ADAI I I I OZ TOX	with Bridgeplane	with Bridgeplane
2 PD3/N 3 PD4/S		PD2/MISO	PD2/MISO	PD2/MISO	PD2/MISO
3 PD4/S	V/( )S1		PD3/MOSI	PD3/MOSI	PD3/MOSI
			PD4/SCK	PD4/SCK	PD4/SCK
4 15 03/3			PD5/SS*	PD5/SS*	PD5/SS*
5 PD1/1		PD1/TXD	PD1/TXD	F D3/33	PD1/TXD
			PA7/PAI/OC1	PA7/PAI/OC1	PA7/PAI/OC1
			PA6/OC2/OC1	PA6/OC2/OC1	PA6/OC2/OC1
			PA5/OC3/OC1	PA5/OC3/OC1	PA5/OC3/OC1
			PA4/OC4/OC1	PA4/OC4/OC1	PA4/OC4/OC1
		PA3/IC4/OC5/OC1	PA3/IC4/OC5/OC1	PA3/IC4/OC5/OC1	PA3/IC4/OC5/OC1
11 PA2/I			PA2/IC1	PA2/IC1	PA2/IC1
12 PA1/I			PA1/IC2	PA1/IC2	PA1/IC2
13 PA0/l			PA0/IC3	PA0/IC3	PA0/IC3
	,		PB7		
		PB6 (ADDR14)	PB6		
			PB5		
		,	PB4		
		,	PB3		
		,	PB2		
	/	PB1 (ADDR9)	PB1		
		,	PB0		
22 PE0/A			PE0/AN0	PE0/AN0	
23 PE1/A			PE1/AN1	PE1/AN1	
24 PE2/A			PE2/AN2	PE2/AN2	
25 PE3/A			PE3/AN3	PE3/AN3	
26 PE7/A			PE7/AN7	PE7/AN7	
27 PE6/A			PE6/AN6	PE6/AN6	
28 PE5/A			PE5/AN5	PE5/AN5	
29 PE4/ <i>F</i>	AN4		PE4/AN4	PE4/AN4	
30 VRH		VRH	VRH	VRH	
31 VRL		VRL	VRL	VRL	
32 STRA		STRA (AS)	STRA		
33 ECLK			ECLK		
			STRB		
			PC0		
			PC1		
			PC2		
		,	PC3		
			PC4		
			PC5		
			PC6		
42 PC7 (			PC7		
			STRB		
44 RESE	T*	RESET*	RESET*		RESET*
45 XIRQ	*	XIRQ*	XIRQ*	XIRQ*	XIRQ*
46 IRQ*			IRQ*	IRQ*	IRQ*
47 VCC	(+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)
48 PD0/F		PD0/RXD	PD0/RXD	·	PD0/RXD
49 GROU		GROUND	GROUND	GROUND	GROUND
50 GROU	UND	GROUND	GROUND	GROUND	GROUND
					VIN

					Rev. 2 (4 Mar 09)	
		MCU CARD H1	1 CONNECTOR SIGNALS			
Pin#	ADAPT812/DX/DXLT/MAX	ADAPT912B32	ADAPT912DT60	AD9S12C	AD9S12DP	
		7.27.1.101.2.02	112111 10122100	7.200.20		
1	PS4/MISO	PS4/SDI/MISO	PS4/SDI/MISO	PM2/MISO	PS4/MISO	
2	PS5/MOSI	PS5/SDO/MOSI	PS5/SDO/MOSI	PM4/MOSI	PS5/MOSI	
3	PS6/SCK	PS6/SCK	PS6/SCK	PM5/SCK	PS6/SCK	
4	PS7/SS*	PS7/SS*	PS7/SS*	PM3/SS*	PS7/SS*	
5	PS1/TXD0	PS1/TXD0	PS1/TXD0	PJ7/PP7	PS1/TXD0	
6	PT7/IOC7/PAI	PT7/IOC7/PAI	PT7/IOC7/PAI	PT7/IOC7/PAI	PT7/IOC7/PAI	
7	PT6/IOC6	PT6/IOC6	PT6/IOC6	PT6/IOC6	PT6/IOC6	
8	PT5/IOC5	PT5/IOC5	PT5/IOC5	PT5/IOC5	PT5/IOC5	
9	PT4/IOC4	PT4/IOC4	PT4/IOC4	PT4/IOC4	PT4/IOC4	
10	PT3/IOC3	PT3/IOC3	PT3/IOC3	PT3/IOC3	PT3/IOC3	
11	PT2/IOC2	PT2/IOC2	PT2/IOC2	PT2/IOC2	PT2/IOC2	
12	PT1/IOC1	PT1/IOC1	PT1/IOC1	PT1/IOC1	PT1/IOC1	
13	PT0/IOC0	PT0/IOC0	PT0/IOC0	PT0/IOC0	PT0/IOC0	
14	PJ7/KWJ7	PJ7/PP7	PJ7/PP7	PB7	PP7/KWP7/PWM7	
15	PJ6/KWJ6	PJ6/PP6	PJ6/PP6	PB6	PP6/KWP6/PWM6	
16	PJ5/KWJ5	PJ5/PP5	PJ5/PP5	PB5	PP5/KWP5/PWM5	
17	PJ4/KWJ4	PJ4/PP4	PJ4/PP4	PB4	PP4/KWP4/PWM4	
18	PJ3/KWJ3	PJ3/PP3/PW3	PJ3/PP3/PW3	PB3	PP3/KWP3/PWM3	
19	PJ2/KWJ2	PJ2/PP2/PW2	PJ2/PP2/PW2	PB2	PP2/KWP2/PWM2	
20	PJ1/KWJ1	PJ1/PP1/PW1	PJ1/PP1/PW1	PB1	PP1/KWP1/PWM1	
21	PJ0/KWJ0	PJ0/PP0/PW0	PJ0/PP0/PW0	PB0	PP0/KWP0/PWM0	
22	PAD0/AN0	PAD0/AN0	PAD0/AN0	AN00	PAD0/AN0	
23	PAD1/AN1	PAD1/AN1	PAD1/AN1	AN01	PAD1/AN1	
24	PAD2/AN2	PAD2/AN2	PAD2/AN2	AN02	PAD2/AN2	
25	PAD3/AN3	PAD3/AN3	PAD3/AN3	AN03	PAD3/AN3	
26	PAD7/AN7	PAD7/AN7	PAD7/AN7	AN07	PAD7/AN7	
27	PAD6/AN6	PAD6/AN6	PAD6/AN6	AN06	PAD6/AN6	
28	PAD5/AN5	PAD5/AN5	PAD5/AN5	AN05	PAD5/AN5	
29	PAD4/AN4	PAD4/AN4	PAD4/AN4	AN04	PAD4/AN4	
30	VRH	VRH	VRH	VRH	VRH	
31	VRL	VRL	VRL	VRL	VRL	
32	PS3/TXD1	PS3	PS3	PE3 (LSTRB*)	PS3/TXD1	
33	PE4/ECLK	PE4/ECLK	PE4/ECLK	PE4/ECLK	PE4/ECLK	
34	PS2/RXD1	PS2	PS2	PE2 (R/W*)	PS2/RXD1	
35	KWH7	NO CONNECTION	NO CONNECTION	PA7	PH7/KWH7	
36	KWH6	PDLC6	PDLC6	PA6	PH6/KWH6	
	KWH5	PDLC5	PDLC5	PA5	PH5/KWH5	
38	KWH4	PDLC4	PDLC4	PA4	PH4/KWH4	
39	KWH3	PDLC3	PDLC3	PA3	PH3/KWH3	
40	KWH2	PDLC2	PDLC2	PA2	PH2/KWH2	
41	KWH1	PDLC1	PDLC1	PA1	PH1/KWH1	
42	KWH0	PDLC0	PDLC0	PA0	PH0/KWH0	
43	PE7/ARST	PE7/DBE*	PE7/DBE*	PE7/XCLKS*	PE7/NOACC/XCLKS*	
44	RESET*	RESET*	RESET*	RESET*	RESET*	
45	XIRQ*	XIRQ*	XIRQ*	PE0/XIRQ*	PE0/XIRQ*	
46	IRQ*	IRQ*	IRQ*	PE1/IRQ*	PE1/IRQ*	
47	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	
48	PS0/RXD0	PS0/RXD0	PS0/RXD0	PJ6	PS0/RXD0	
49	GROUND	GROUND	GROUND	GROUND	GROUND	
50	GROUND	GROUND	GROUND	GROUND	GROUND	
			_			

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	MC	U CARD H1 CONNE		
Pin#	AD9S12E	ADAPT9S12NE	AD9S12XDP	
	7.500.122	(3V I/O PINS)		
1	PS4/MISO	PS4/MISO	PS4/MISO0	
2	PS5/MOSI	PS5/MOSI	PS5/MOSI0	
3	PS6/SCK	PS6/SCK	PS6/SCK0	
4	PS7/SS	PS7/SS	PS7/SS0*	
5	PS1/TXD0	PS1/TXD0	PS1/TXD0	
6	PT7/IOC17	PT7/TIM IOC7	PT7/IOC7	
7	PT6/IOC16	PT6/TIM_IOC6	PT6/IOC6	
8	PT5/IOC15	PT5/TIM IOC5	PT5/IOC5	
9	PT4/IOC14	PT4/TIM_IOC4	PT4/IOC4	
10	PT3/IOC7	NO CONNECTION	PT3/IOC3	
11	PT2/IOC6	NO CONNECTION	PT2/IOC2	
12	PT1/IOC5	NO CONNECTION	PT1/IOC1	
13	PT0/IOC4	NO CONNECTION	PT0/IOC0	
14	PM1/DA1	PG7/KWG7	PP7/KWP7/PWM7/SCK2	
15	PM0/DA0	PG6/KWG6	PP6/KWP6/PWM6/SS2*	
16	PP5/PW05	PG5/KWG5	PP5/KWP5/PWM5/MOSI2	
17	PP4/PW04	PG4/KWG4	PP4/KWP4/PWM4/MISO2	
18	PP3/PW03	PG3/KWG3	PP3/KWP3/PWM3/SS1*	
19	PP2/PW02	PG2/KWG2	PP2/KWP2/PWM2/SCK1	
20	PP1/PW01	PG1/KWG1	PP1/KWP1/PWM1/MOSI1	
21	PP0/PW00	PG0/KWG0	PP0/KWP0/PWM0/MISO1	
22	PAD00/AN00/KWAD00	PAD0/AN0	PAD00/AN00	
23	PAD01/AN01/KWAD01	PAD1/AN1	PAD01/AN01	
24	PAD02/AN02/KWAD02	PAD2/AN2	PAD02/AN02	
25	PAD03/AN03/KWAD03	PAD3/AN3	PAD03/AN03	
26	PAD07/AN07/KWAD07	PAD7/AN7	PAD07/AN07	
27	PAD06/AN06/KWAD06	PAD6/AN6	PAD06/AN06	
28	PAD05/AN05/KWAD05	PAD5/AN5	PAD05/AN05	
29	PAD04/AN04/KWAD04	PAD4/AN4	PAD04/AN04	
30	VRH	VRH	VRH	
31	VRL	VRL	VRL	
32	PS3/TXD1	PS3/TXD1	PS3/TXD1	
33	PE4/ECLK	PE4/ECLK	PE4/ECLK	
34	PS2/RXD1	PS2/RXD1	PS2/RXD1	
35	PU7	NO CONNECTION	PH7/KWH7/SS2*/TXD5	
36	PU6	PH6/KWH6/MII_TXER	PH6/KWH6/SCK2/RXD5	
37	PU5/PW15	PH5/KWH5/MII_TXEN	PH5/KWH5/MOSI2/TXD4	
38	PU4/PW14		PH4/KWH4/MISO2/RXD4	
39	PU3/PW13/IOC27	PH3/KWH3/MII_TXD3	PH3/KWH3/SS1*	
40	PU2/PW12/IOC26	PH2/KWH2/MII_TXD2	PH2/KWH2/SCK1	
41	PU1/PW11/IOC25	PH1/KWH1/MII_TXD1	PH1/KWH1/MOSI1 PH0/KWH0/MISO1	
42 43	PU0/PW10/IOC24 PE7/NOACC/XCLKS*	PH0/KWH0/MII_TXD0 PE7/NOACC	PE7/XCLKS*	
43	RESET*	RESET*	RESET*	
44	PE0/XIRQ*	PE0/XIRQ*	PE0/XIRQ*	
45	PE1/IRQ*	PE1/IRQ*	PE1/IRQ*	
46	VCC (+5V)	VCC (+3V)	VCC (+5V)	
48	PS0/RXD0	PS0/RXD0	PS0/RXD0	
49	GROUND	GROUND	GROUND	
50	GROUND	GROUND	GROUND	
30	CITOOIND	CINOUND	CICOUND	
	1	1		

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		APPLICA	TION CARD CON	ION CARD CONNECTOR P1		
Pin#	AD9S12DEMH1	DKKI	AD11DXVRPM	AD11QMDM	AD9S12SSMI	
				1		
1	LCD DB4			MDIRA2	MISO	
2	LCD DB5	595DAT		MDIRB2	MOSI	
3	LCD DB6	595CLK		MENA2	SCK	
4	LCD DB7	595LAT		MENB2	SS*	
5	LED D2 GRN					
6	S1	KBD DAT		MOSFET		
7	S2	SPKR		MDIRB1		
8	S3			MDIRA1		
9	S4			MENB1		
10	S5	KBD CLK		MENA1		
11	S6				SPEAKER	
12	S7				SRF04 PIN3	
13	S8		EOM*		SRF04 PIN2	
14	SPKR	DISP EN	A7			
15	LCD CONTRAST	DISP RS	A6			
16	VOUT		A5			
17	DRIVER1/SEG8		A4			
18	DRIVER2/SEG9		A3		SERVO4	
19	LCD RS		A2		SERVO3	
20	LCD E		A1		SERVO2	
21	LCD R/W*		A0		SERVO1	
22					SERVO2 MONITOR	
23	TEMP				SERVO1 MONITOR	
24	POT				VIN MONITOR	
25	LIGHT				MIC	
26	SW5				SENSOR4	
27	SW4				SENSOR3	
28	SW3				SENSOR2	
29	SW2				SENSOR1	
30						
31	AGND					
32					TX SCI	
33						
34					RX SCI	
35	LED SEG7	KEYY3			IR TX GATE	
36	LED SEG6	KEYY2				
37	LED SEG5	KEYY1				
38	LED SEG4	KEYY0	PD			
39	LED SEG3	KEYX3	A8			
40	LED SEG2	KEYX2	A9			
41	LED SEG1	KEYX1	P/R*			
42	LED SEG0	KEYX0	CE*			
43						
44						
45		LOTO			ID DV	
46	) (OO ( 5) °	KSTRB	1/00 / 710	) (OO ( =) ()	IR RX	
47	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	
48	LED D3 RED	LED D3 RED	00011110	0001	0001110	
49	GROUND	GROUND	GROUND	GROUND	GROUND	
50	GROUND	GROUND	GROUND	GROUND	GROUND	
	1					

						Rev. 2 (4 Mar 09)
		APPLIC	ATION CAR	D CONNECT	OR P1	,
Pin#	AD12XYSM	AD12DXXYZSM	AD12DAC4	AD12DAS32	AD11MIB	AD11MX1
1			WR*			
2					MOSI/SDA	
3			CSLSB*		SCK/SCL	
4			CSMSB*		SS*	
5						
6				HBEN		
7	MOSFET			PT6		
8				PT5	IR TX GATE	
9					SPEAKER	
10						
11		X HOME				
12		Y HOME				
13	V0.14.0TD1	XZ HOME	\	00.45*	10001-	1000/-
14	X0 I1 CTRL	Y HALF	YLDAC4	CS4B*	ADDR15	ADDR15
15	X0 I0 CTRL	Z HALF	XLDAC4	CS4A*	ADDR14	ADDR14
16	X1 I1 CTRL	X DIR	YLDAC3	CS3B*	ADDR13	ADDR13
17	X1 I0 CTRL	X STEP	XLDAC3	CS3A*	ADDR12	ADDR12
18	Y0 I1 CTRL	Y STEP	YLDAC2	CS2B*	ADDR11	ADDR11
19	Y0 I0 CTRL	Y DIR	XLDAC2	CS2A*	ADDR10	ADDR10
20	Y1 I1 CTRL	Z STEP	YLDAC1	CS1B*	ADDR9	ADDR9
21	Y1 I0 CTRL	Z DIR	XLDAC1	CS1A*	ADDR8	ADDR8
22				INT3A		
23				INT3B	LIGHT	
24				INT4A	TEMP	
25				INT4B	POT	
26				INT2B	DACA/J5-1	
27				INT2A	DACB/J5-3	
28				INT1B	DACC/J5-5	
29				INT1A	DACD/J5-7	
30					VRH OUT	
31					AGND	4.0
32				FOLK	AS	AS
33				ECLK	E	E
34	LIMIT INDUIT VA	VIIALE	D0/D0	D.7	R/W*	R/W*
35	LIMIT INPUT Y1	X HALF	D0/D8	D7	ADDRO/DATAO	ADDRO/DATA0
36	LIMIT INPUT YO	Z CTRL	D1/D9	D6	ADDRO/DATAO	ADDR1/DATA1
37	LIMIT INPUT X1	Y CTRL	D2/D10	D5	ADDR2/DATA2	ADDR2/DATA2
38	LIMIT INPUT X0	X CTRL	D3/D11	D4	ADDR3/DATA3	ADDR3/DATA4
39	Y PHASE CTRL1	XYZ EN	D4	D3	ADDR4/DATA4	ADDR4/DATA4
40	Y PHASE CTRL0	X LIMIT	D5	D2	ADDR6/DATA6	ADDRE/DATAS
41	X PHASE CTRL1	Y LIMIT Z LIMIT	D6 D7	D1/D9	ADDR6/DATA6 ADDR7/DATA7	ADDRZ/DATAZ
42	X PHASE CTRL0	∠ LIIVII I	וטו	D0/D8	AUUK//UATA/	ADDR7/DATA7
43 44		RESET*			RESET*	
44		INESET		<del> </del>	SW3 (XIRQ*)	
	LIMITO	LIMITO				
46 47	LIMITS VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	SW4 (IRQ*) VCC (+5V)	VCC (+5V)
48	VOO (+3V)	VUU (+3V)	VUU (+3V)	V CC (+3V)	v CC (+3v)	v CC (+3v)
48	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
50	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
30	CIVOOND	טויטטויט	CINCOIND	CINOUND	CINOUND	CINOUND
	I .	L	L	L	<u> </u>	J.