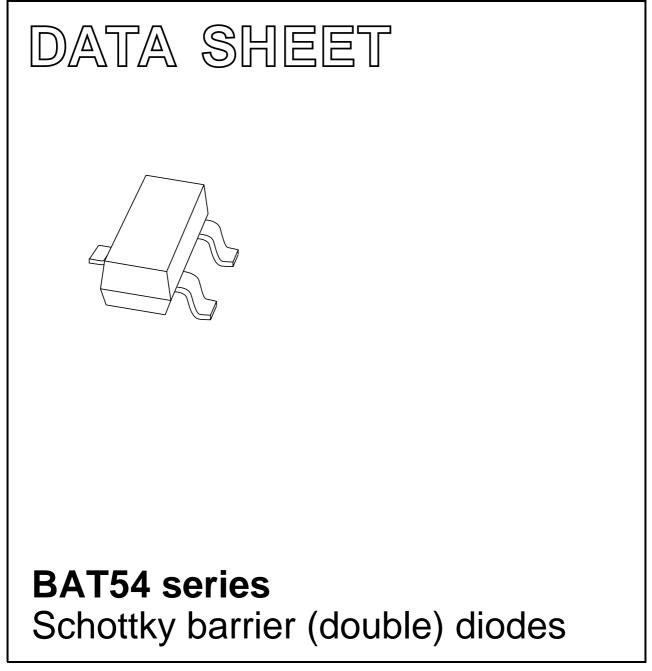
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 2001 Oct 12 2002 Mar 04



FEATURES

- Low forward voltage
- · Guard ring protected
- Small plastic SMD package.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- Protection circuits
- Blocking diodes.

DESCRIPTION

Planar Schottky barrier diodes encapsulated in a SOT23 small plastic SMD package. Single diodes and double diodes with different pinning are available.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BAT54	L4*
BAT54A	L42 or *V3
BAT54C	L43 or *W1
BAT54S	L44 or *V4

Note

- 1. * = p : Made in Hong Kong.
 - * = t : Made in Malaysia.
 - * = W: Made in China.

PINNING

DIN	DESCRIPTION			
PIN	BAT54	BAT54A	BAT54C	BAT54S
1	а	k ₁	a ₁	a ₁
2	n.c.	k ₂	a ₂	k ₂
3	k	a ₁ , a ₂	k ₁ , k ₂	k ₁ , a ₂

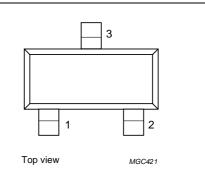
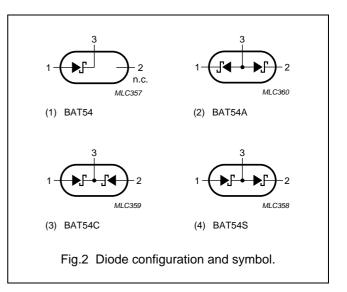


Fig.1 Simplified outline (SOT23) and pin configuration.



BAT54 series

BAT54 series

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _R	continuous reverse voltage		_	30	V
I _F	continuous forward current		-	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \delta \le 0.5$	-	300	mA
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms	-	600	mA
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	125	°C
Per device					
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	_	230	mW

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

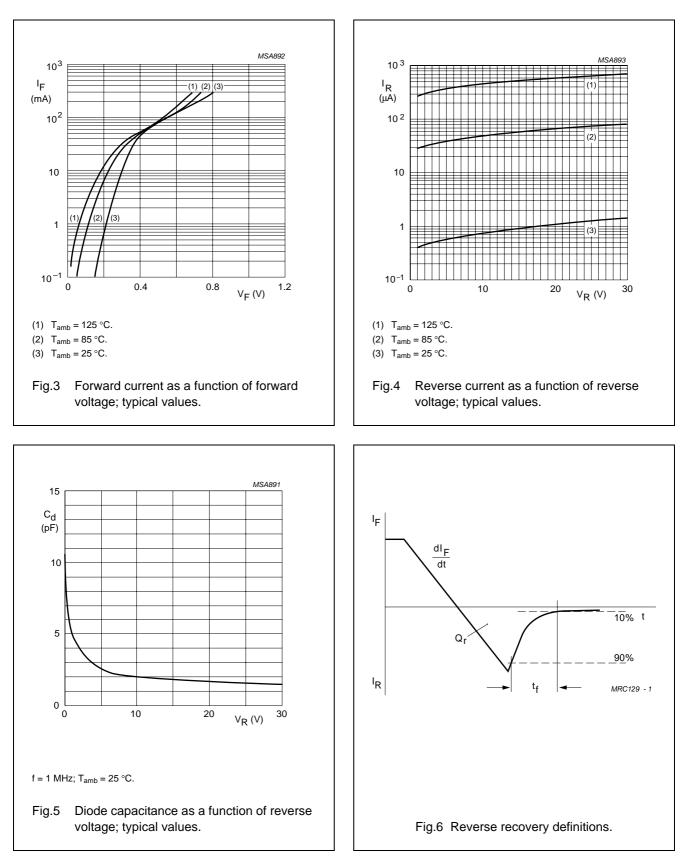
1. Refer to SOT23 standard mounting conditions.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
Per diode				
V _F	forward voltage	see Fig.3		
		I _F = 0.1 mA	240	mV
		I _F = 1 mA	320	mV
		I _F = 10 mA	400	mV
		I _F = 30 mA	500	mV
		I _F = 100 mA	800	mV
I _R	reverse current	V _R = 25 V; see Fig.4	2	μA
t _{rr}	reverse recovery time	when switched from $I_F = 10 \text{ mA}$ to $I_R = 10 \text{ mA}$; $R_L = 100 \Omega$; measured at $I_R = 1 \text{ mA}$; see Fig.6	5	ns
C _d	diode capacitance	$f = 1 \text{ MHz}; V_R = 1 \text{ V}; \text{ see Fig.5}$	10	pF

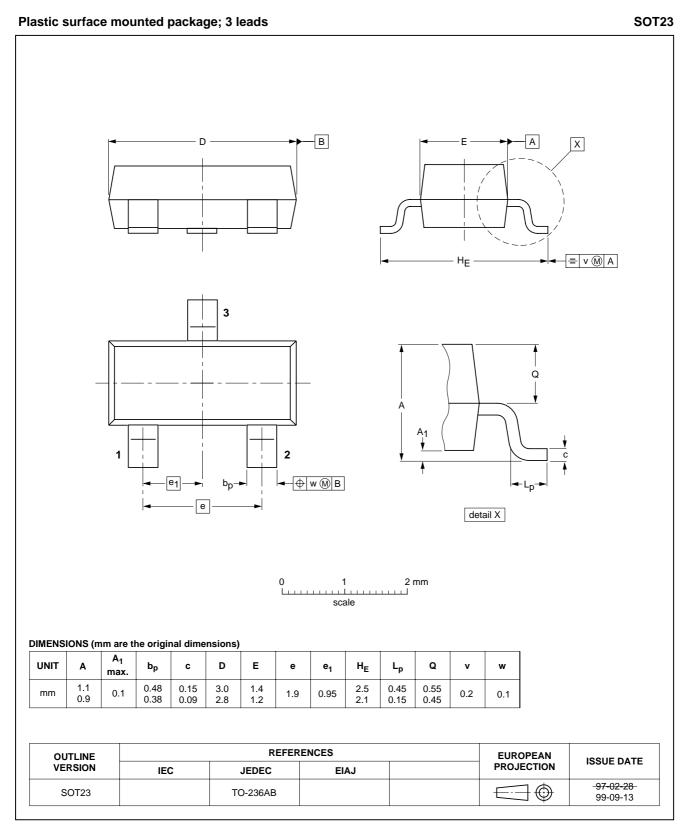
BAT54 series



BAT54 series

Schottky barrier (double) diodes

PACKAGE OUTLINE



BAT54 series

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

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