

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Drain-source voltage	$\pm V_{DS}$	max.	30 V
Drain-gate voltage (open source)	V_{DGO}	max.	30 V
Gate-source voltage (open drain)	$-V_{GSO}$	max.	30 V
Gate current	I_G	max.	10 mA
Total power dissipation up to $T_{amb} = 75^\circ\text{C}$	P_{tot}	max.	300 mW
up to $T_{amb} = 90^\circ\text{C}$	P_{tot}	max.	300 mW (1)
Storage temperature	T_{sg}	max.	-65 to +150 $^\circ\text{C}$
Junction temperature	T_j	max.	150 $^\circ\text{C}$

THERMAL RESISTANCE

From junction to ambient in free air	R_{thj-a}	=	0,25 K/mW
From junction to ambient	R_{thj-a}	=	0,20 K/mW (1)

CHARACTERISTICS $T_{amb} = 25^\circ\text{C}$ unless otherwise specified

Gate cut-off current

$-V_{GS} = 20 \text{ V}; V_{DS} = 0$

$-I_{GSS}$

$< 5 \text{ nA}$

Drain current (2)

$V_{DS} = 15 \text{ V}; V_{GS} = 0$

Gate-source breakdown voltage

$-I_G = 1 \mu\text{A}; V_{DS} = 0$

Gate-source voltage

$I_D = 200 \mu\text{A}; V_{DS} = 15 \text{ V}$

$-V_{(BR)GSS} > 30 \text{ V}$

$-V_{GS(3)} > 0,5 \text{ to } 7,5 \text{ V}$

y-parameters (common source)			
Transistor admittance at $f = 1 \text{ kHz}$	$ Y_{fs} $	> typ.	4,5 mS (1)
$V_{DS} = 15 \text{ V}; V_{GS} = 0$		5 mS	
Output capacitance at $f = 1 \text{ MHz}$	C_{os}	typ.	1,2 pF
$V_{DS} = 20 \text{ V}; V_{GS} = 0$			
Feedback capacitance at $f = 1 \text{ MHz}$	C_{rs}	typ.	0,7 pF
$V_{DS} = 20 \text{ V}; -V_{GS} = 1 \text{ V}$			
Cut-off frequency	f_{fs}	typ.	1 GHz (2)
$V_{DS} = 15 \text{ V}; V_{GS} = 0$			
Noise figure at $f = 800 \text{ MHz}$	F	typ.	7,5 dB
$V_{DS} = 10 \text{ V}; R_S = 47 \Omega$			
Power gain at $f = 800 \text{ MHz}$	G_p	typ.	11 dB
$V_{DS} = 15 \text{ V}; R_S = 47 \Omega$			

1) Transistor mounted on printed circuit board, maximum lead length 3 mm, mounting pad for drain lead minimum 10 mm \times 10 mm.2) Measured under pulse conditions: $t_p = 300 \mu\text{s}; \delta \leq 0,02$.3) BF256B/1: $I_{DS} = 6$ to 8 mA; $-V_{GS} = 1,4$ to 2,6 V.