





WinPic800 - v 3.59

File Edit Device Utilities Settings Language Help



Code Data Setting

on/off

- Oscillator -

HS oscillator, PLL enabled , HS used by USB

-Watchdog-

1:32768

- config -

-CPU System Clock-

96 MHz PLL div 2

-Oscillator Selection-

Divided by 5 (20 MHz input)

☒ WRTB

1 : 0E24

☒ WRT0

2 : 1E38

☐ WRT1

3 : 0100

☐ WRT2

4 : 0085

☐ WRT3

5 : C00F

☐ WRTC

6 : A00E

☐ WRTD

7 : 400F

☐ FCMEM

☒ CCP2MX

☒ STVREN

☐ CPB

☐ EBTRB

☐ IESO

☐ PBADEN

☒ LVP

☐ CP0

☐ EBTR0

☐ LPT1OSC

☐ XINST

☐ CP1

☐ EBTR1

☐ WD TEN

☐ MCLRE

☐ ICPORT

☐ CP2

☐ EBTR2

☒ PWR TEN

☐ DEBUG

☐ CP3

☐ EBTR3

☐ CPD

☐ WRTC

☒ USBPLL

☐ WRTD

☒ VREGEN

- ID -

FFFF FFFF FFFF FFFF

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