

Determine the Thevenin equivalent of the circuit in Fig. 9-42(a) as seen from the terminals A-B.

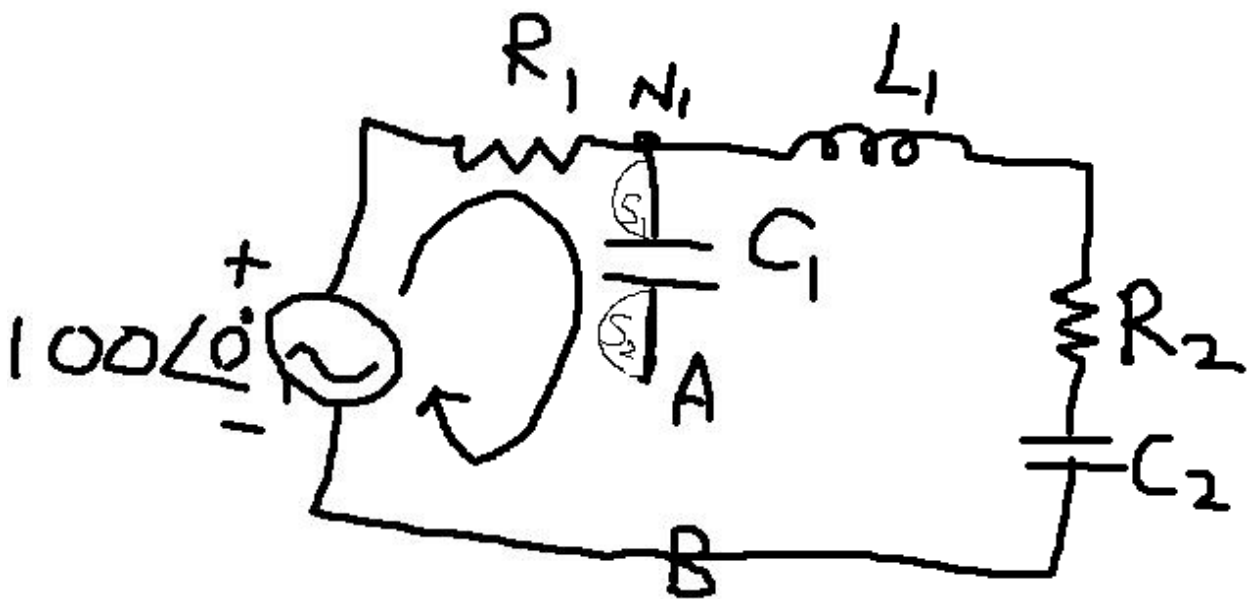
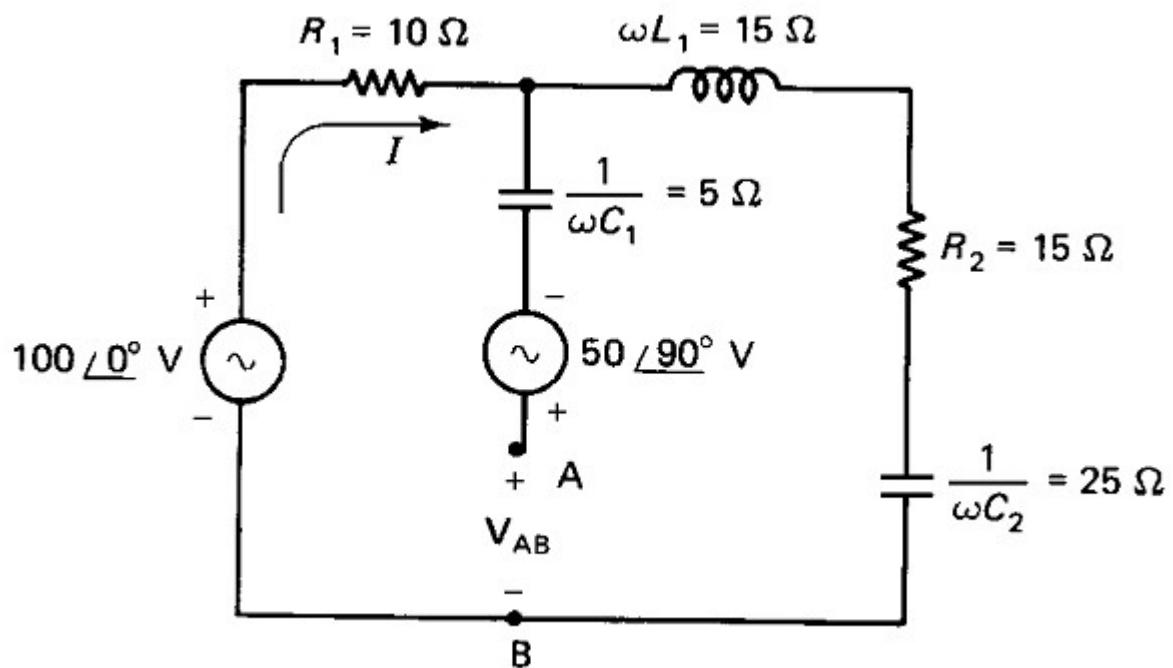


fig.1 If I apply KVL around the left-hand loop, the voltage across capacitor C_1 is zero because there is no current flowing through it? (I have removed the voltage source between C_1 and terminal A)