

# **S1A - S1M**

### **Features**

- Low profile package.
- Glass passivated junction.



# **General Purpose Rectifiers**

## Absolute Maximum Ratings\* T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value			Units				
		1A	1B	1D	1G	1J	1K	1M	
$V_{RRM}$	Maximum Repetitive Reverse Voltage	50	100	200	400	600	800	1000	V
I <sub>F(AV)</sub>	Average Rectified Forward Current, @ T <sub>A</sub> = 100°C	1.0				Α			
I <sub>FSM</sub>	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave			Α					
T <sub>stg</sub>	Storage Temperature Range -55 to +150			°C					
T <sub>J</sub>	Operating Junction Temperature -55 to +150			°C					

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### **Thermal Characteristics**

Symbol	Parameter	Value	Units
$P_{D}$	Power Dissipation	1.4	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient*	85	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 0.013 mm.

# **Electrical Characteristics** $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Device			Units				
		1A	1B	1D	1G	1J	1K	1M	1
V <sub>F</sub>	Forward Voltage @ 1.0 A		1.1					V	
t <sub>rr</sub>	Reverse Recovery Time $I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$				1.8				μs
I <sub>R</sub>	Reverse Current @ rated $V_R$ $T_A = 25^{\circ}C$ $T_A = 125^{\circ}C$				1.0 50				μA μA
Ст	Total Capacitance V <sub>R</sub> = 4.0 V, f = 1.0 MHz				12				pF

### **General Purpose Rectifiers**

(continued)

## **Typical Characteristics**

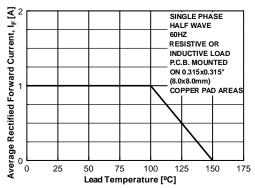


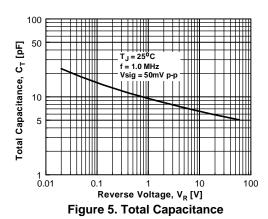
Figure 1. Forward Current Derating Curve

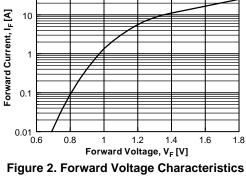
8.3ms Single Half Sine-Way

JEDEC Method



Number of Cycles at 60Hz
Figure 3. Non-Repetitive Surge Current





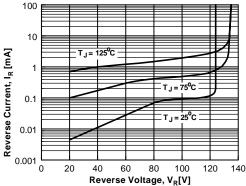


Figure 4. Reverse Current vs Reverse Voltage

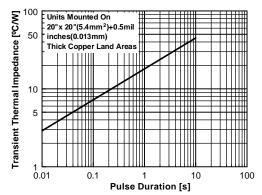


Figure 6. Thermal Impedance Characteristics

#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

SMART START™  $VCX^{TM}$ FAST ® OPTOLOGIC™ STAR\*POWER™ FASTr™ Bottomless™ OPTOPLANAR™ Stealth™ CoolFET™ FRFET™ PACMAN™ SuperSOT™-3 CROSSVOLT™ GlobalOptoisolator™ POP™ SuperSOT™-6 DenseTrench™ GTO™ Power247™  $HiSeC^{TM}$ SuperSOT™-8  $Power Trench^{\, @}$ DOME™ SyncFET™ EcoSPARK™ ISOPLANAR™ QFET™ TinyLogic™ E<sup>2</sup>CMOS<sup>TM</sup> LittleFET™  $OS^{TM}$ 

QT Optoelectronics™

MicroFET™

STAR\*POWER is used under license

#### DISCLAIMER

EnSigna™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

TruTranslation™

#### PRODUCT STATUS DEFINITIONS

### **Definition of Terms**

Datasheet Identification Product Status		Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. H4