

$$K = \frac{1 + jf/f_H}{g_m R_T} \frac{1}{1 + jf/f_H} = \frac{1}{K_0} \quad (11-59)$$

Solving for $V_o/V_i = K$, we obtain

$$V_o = \frac{1/R_T + j\omega C_L R_T}{g_m A_{v_e} - V_o} = \frac{1 + j\omega C_L R_T}{g_m R_T (A_v - V_o)} \quad (11-58)$$

$K = 1$, we obtain

determined, to a good approximation, by the output circuit alone. Using load is highly capacitive, then $\tau_o \ll \tau_i$. Hence the upper 3-dB frequency is put time constant τ_o is proportional to C_L , and since we have assumed that the and $1 - K \approx 0$. Hence the input time constant $\tau_i \approx (R_s + r_{be})C_o$. The output low-frequency gain of an emitter follower is close to unity: $K \approx 1$

Fig. 11-14b. With $K = V_o/V_i$, we obtain the circuit of Fig. 11-15.

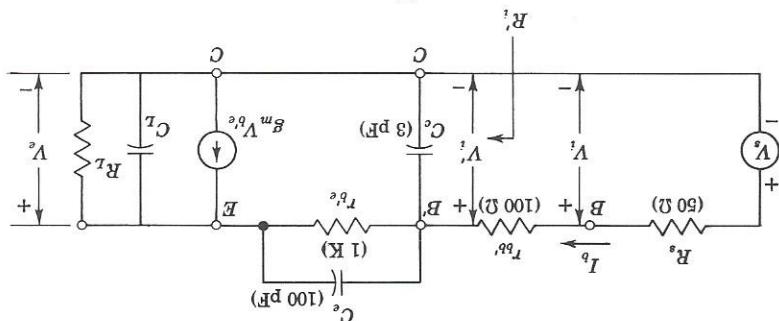
Fig. 11-14a. We can obtain a very simple approximate expression for the transfer function by applying Miller's theorem to the circuit of

Single-pole Solution We can obtain a very simple approximate expres-

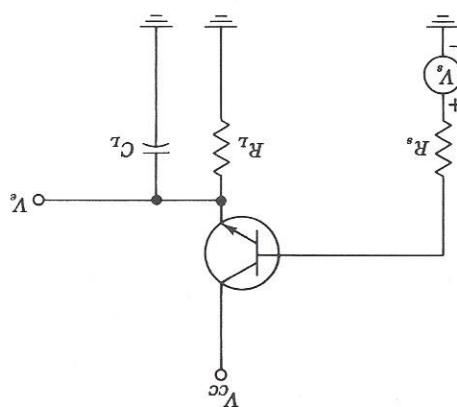
circuit of emitter follower.

Fig. 11-14 (a) Emitter follower. (b) High-frequency equivalent

(b)



(a)



where

$$K_o = \frac{g_m R_L}{1 + g_m R_L} \approx 1 \quad (11-60)$$

and

$$f_H = \frac{1 + g_m R_L}{2\pi C_L R_L} \approx \frac{g_m}{2\pi C_L} = \frac{f_T C_e}{C_L} \quad (11-61)$$

and f_T is given by Eq. (11-30). Since $f_H = 1/2\pi\tau_o$, we see that $\tau_o = C_L/g_m$, and the condition $\tau_o \gg \tau_i$ requires

$$C_L \gg g_m(R_s + r_{bb'})C_c \quad (11-62)$$

For the parameter values in Fig. 11-14 and $g_m = 50 \text{ mA/V}$, this condition is $C_L \gg (50)(150)(3) = 23 \text{ pF}$.

Since the input impedance between terminals B' and C is very large compared with $R_s + r_{bb'}$, then K also represents the overall voltage gain $A_{V_s} \equiv V_e/V_s$. Incidentally, a somewhat better approximation for f_H is given in Prob. 11-20, where we find

$$f_H = \frac{g_m + g_{b'e}}{2\pi(C_L + C_e)} \quad (11-63)$$

Input Admittance We can find the input admittance (excluding $r_{bb'}$) by referring to Fig. 11-15.

$$Y'_i = \frac{I_b}{V'_i} = j\omega[C_c + (1 - K)C_e] + (1 - K)g_{b'e}$$

Substituting K from Eq. (11-59) in this equation, we find

$$Y'_i = j2\pi f C_e + (g_{b'e} + j2\pi f C_e) \frac{1 - K_o + jf/f_H}{1 + jf/f_H} \quad (11-64)$$

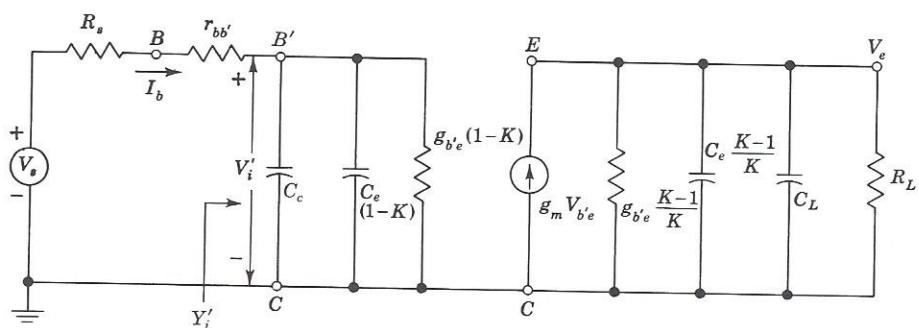


Fig. 11-15 The equivalent circuit of the emitter follower, using Miller's theorem.

- 11-4 How does g_m vary with (a) $|I_c|$; (b) $|V_{ce}|$; (c) T ?
- 11-3 What is the order of magnitude of each resistance in the hybrid- π model?
- 11-2 (a) What is the physical origin of the two capacitors in the hybrid- π model?
 (b) What is the order of magnitude of each capacitance?
- 11-1 Draw the small-signal high-frequency CE model of a transistor.

REVIEW QUESTIONS

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One way to remedy this condition is to use a small resistance in series with R_s . This, the input impedance consists of a capacitance shunted by a negative resistance and it the source resistance R_s contains some inductance in frequency. Thus, the last term represents a negative resistance which is a function of frequency. Here the last term represents a negative resistance which is to use a small resistance in series with R_s .

$$(11-65)$$

$$Y_i \approx j2\pi fC_o + (g_{o\theta} + j2\pi fC_o) \left(1 - K_o + \frac{f_H}{f} \right) - 2\pi f \frac{f_H}{C_o}$$

$$\approx j2\pi fC_o + g_{o\theta} (1 - K_o) + jg_{o\theta} \frac{f_H}{f} - 2\pi f \frac{f_H}{C_o}$$

Since $K_o \approx 1$, the numerator of Eq. (11-64) is affected by frequency at a much lower value of f than is the denominator. Hence, for $f < f_H$, Eq. (11-64) can be written