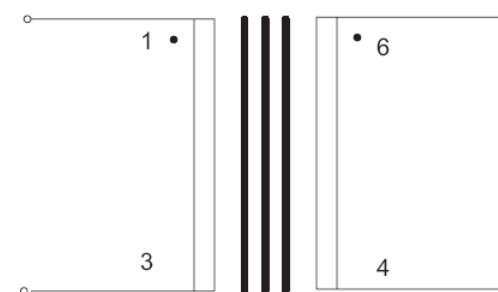


X ZZ = date code

Pin diameter = 0.6mm

Pin view



Electrical schematic

Specification:

Primary DC resistance: 65 ohms +/- 15%
 Secondary DC resistance: 65 ohms +/- 15%
 Impedance matching: 600 ohms to 600 ohms
 Inductance (10mVrms, 200Hz parallel) pins 1 - 3: 4.15H min.
 Leakage inductance (10mVrms, 200Hz series) pins 1 - 3: 20mH max.
 Return loss: (ref. 600 ohms) 300 to 4kHz: -23dB min.
 Insertion loss: (@1kHz, 1Vrms): 1.3dB max.
 Frequency response (reference 1kHz): 300 - 4kHz (+/- 0.2dB)
 Longitudinal balance: 200Hz - 4kHz: 80dB min.
 Turns ratio (@ 6kHz, 0.1Vrms) pins 1 - 3 & 6 - 4: 1 to 1 (+/- 1%)
 Distortion: 300Hz, 0dBm: -77dB max.
 Saturation: 10V rms (65V peak)
 Signal to noise ratio: >33dB
 Hi-pot, primary to secondary: 4kV, 1mA for 1 minute
 Core loss (@1kHz, 1Vrms): 13k ohms
 Note: Do not pass DC current through windings.

Operating temperature range: 0 to + 70°C
 Storage temperature range: -40°C to +125°C

Tolerance on all dimensions +/-0.2mm unless stated otherwise



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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER	
Specification for OEP1200	1	31/07/03	CS		OEP1200	
	6	07/11/05	CS			
	7	10/03/08	CS			
Scale: 2 to 1	All dimensions in mm unless stated otherwise		8	15/10/08		CS