FAIRCHILD

TIP41 Series(TIP41/41A/41B/41C)

Medium Power Linear Switching Applications

Complement to TIP42/42A/42B/42C



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

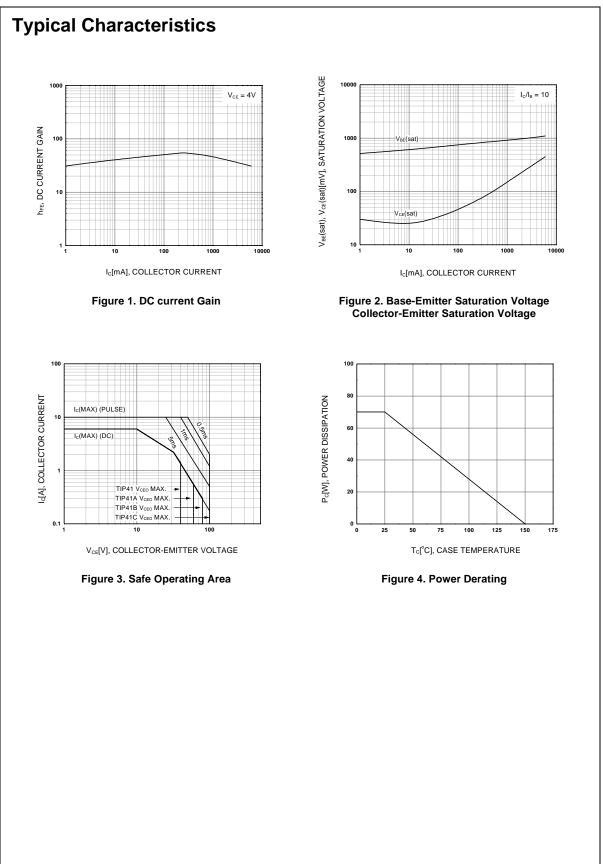
Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Emitter Voltage: TIP41	40	V
	: TIP41A	60	V
	: TIP41B	80	V
	: TIP41C	100	V
V _{CEO}	Collector-Emitter Voltage: TIP41	40	V
	: TIP41A	60	V
	: TIP41B	80	V
	: TIP41C	100	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	6	A
I _{CP}	Collector Current (Pulse)	10	А
I _B	Base Current	2	A
P _C	Collector Dissipation (T _C =25°C)	65	W
P _C P _C	Collector Dissipation (T _a =25°C)	2	W
Т _Ј	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

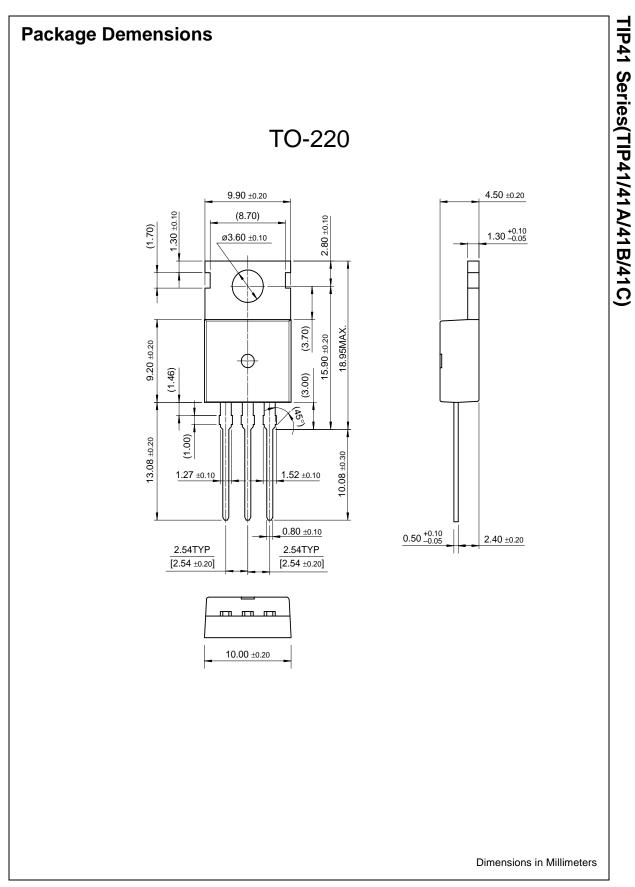
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage				
020	: TIP41	$I_{\rm C} = 30 {\rm mA}, I_{\rm B} = 0$	40		V
	: TIP41A	-	60		V
	: TIP41B		80		V
	: TIP41C		100		V
I _{CEO}	Collector Cut-off Current				
	: TIP41/41A	$V_{CE} = 30V, I_{B} = 0$		0.7	mA
	: TIP41B/41C	$V_{CE} = 60V, I_B = 0$		0.7	mA
I _{CES}	Collector Cut-off Current				
020	: TIP41	$V_{CE} = 40V, V_{EB} = 0$		400	μΑ
	: TIP41A	$V_{CE} = 60V, V_{EB} = 0$		400	μΑ
	: TIP41B	$V_{CE} = 80V, V_{EB} = 0$		400	μΑ
	: TIP41C	$V_{CE} = 100V, V_{EB} = 0$		400	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$		1	mA
h _{FE}	* DC Current Gain	$V_{CE} = 4V, I_{C} = 0.3A$	30		
		$V_{CE} = 4V, I_{C} = 3A$	15	75	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	$I_{\rm C} = 6A, I_{\rm B} = 600 {\rm mA}$		1.5	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	$V_{CE} = 4V, I_C = 6A$		2.0	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 500mA$	3.0		MHz
Pulse Test: PW≤3	00μs, Duty Cycle≤2%	÷	•	•	•

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