

```

1 post114 code
2
3 // swordfish basic three-channel software pwm using tmr2 and adc inputs
4 device = 18f4520
5 clock = 32
6
7 include "intosc.bas"
8 // do not include setdigitalio
9 // to use portb for the adc all pins must be in analog mode due to the way
10 // these old pics map adc inputs
11 dim test as porte.0
12 dim z as byte
13 include "adc.bas"
14 dim x as byte
15 // adc pot inputs
16 // used to set the pwm duty cycle for pwm1-pwm3
17 // connect pot upper lug = vdd, lower lug = gnd, wiper = port io
18 dim
19     pot1 as portb.0,    // rb0/an12
20     pot2 as portb.1,    // rb1/an10
21     pot3 as portb.2     // rb2/an8
22
23 // adc channels
24 const
25     ch_pot1 = 12,        // an12
26     ch_pot2 = 10,        // an10
27     ch_pot3 = 8          // an8
28
29 // pwm outputs
30 dim
31     pwm1 as porta.0,
32     pwm2 as porta.1,
33     pwm3 as porta.2
34
35 // pwm duty cycles (from adc) 0=min, 255=max
36 dim
37     pwm1_duty as byte,
38     pwm2_duty as byte,
39     pwm3_duty as byte
40
41 dim pwm_period as byte
42
43 // pwm timer tmr2
44 dim
45     tmr2if as pir1.bits(1),
46     tmr2ie as pie1.bits(1),
47     tmr2on as t2con.bits(2)
48
49 // set io pin directions and initial settings
50 sub initio()
51     // set inputs
52     input(pot1)
53     input(pot2)
54     input(pot3)
55
56     // set outputs (low to start)
57     low(pwm1)
58     low(pwm2)
59     low(pwm3)
60 end sub
61
62 // pwm tmr2 interrupt
63 interrupt tmr2_isr()
64     tmr2if = 0
65     pwm_period = pwm_period + 1
66     if (pwm_period >= pwm1_duty) then
67         pwm1 = 0
68     else
69         pwm1 = 1

```

```

70     endif
71     if (pwm_period >= pwm2_duty) then
72         pwm2 = 1
73     else
74         pwm2 = 0
75     endif
76     if (pwm_period >= pwm3_duty) then
77         pwm3 = 1
78     else
79         pwm3 = 0
80     endif
81 end interrupt
82
83
84 main:
85 for z = 0 to 5
86 test = 1
87 delayms(500)
88 test = 0
89 delayms(500)
90 next
91 initio()
92
93 // adc setup
94 adcon1 = $00          // all pins set to analog mode, vref = vdd/gnd
95 adcon2 = adc.frc      // adc clock = frc
96 adc.adfm = 0          // left justify (we only use the 8 msb's)
97 adc.setacqtime(100)  // 100us delay
98
99 pwm_period = 0
100 pwm1_duty = 0
101 pwm2_duty = 0
102 pwm3_duty = 0
103
104 // setup pwm timer tmr2
105 // 25khz = 40us/bit -> 40us x 256 = 10240us period, ~10ms period (100hz)
106 t2con = %00000001    // t2outps<3:0>=%000 (1:1), tmr2on=0, t2ckps<1:0>=%01 (1:4)
107 pr2 = 176
108 tmr2 = 0
109 tmr2if = 0
110 tmr2ie = 1
111 tmr2on = 1
112
113 enable(tmr2_isr)
114
115 while (true)
116
117
118     pwm1_duty = adc.read(ch_pot1) >> 8
119     pwm2_duty = adc.read(ch_pot2) >> 8
120     pwm3_duty = adc.read(ch_pot3) >> 8
121 end while
122

```