



RB seems to be data out. RD7, 6, 5 are toggled low momentarily in that sequence, followed by RC1 going high (toggled high then low). Toggle times seem to be 5ms low, then 5ms before next pin is toggled. RC1 is reversed. (10ms cycle time per character). This is assuming 4Mhz clock. If clock is higher, like 8Mhz, then times will be 1/2 of those listed, etc...

For RD7, character at 0x2F is output to PortB. For RD6. Character at 0x30 is sent to PortB, RD5 going low outputs character at 0x31. RC1 going high outputs value at 0x32. Not sure why RC1 is inverted, maybe bug in disassembly?

All output characters listed above are initialized as 0xF0.

On startup, the outputs cycled once, then turns on RC0, which seems to stay on. After, the output cycles as listed above over and over.